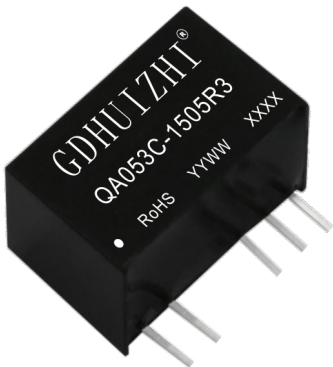


Typical Characteristics

Power Module for SIC MOSFET Drivers

- Isolation voltage: 3000VAC
- Low no-load power consumption:
0.025W(Typ.)
- Efficiency: up to 87% (Typ.)
- Operating ambient temperature: -40℃~+105℃
- MTBF≥3.5 million hours (3500000Hrs)
- Output short-circuit protection:
Continuous short-circuit protection
automatic recovery
- Compact SIP package, plastic housing
- Industry standard pin-out
- Ripple/noise (20MHz bandwidth):
30mVp-p(Typ.)



QAxx3C-R3 series is a DC-DC module power supply designed by Huizhi Electronics specifically for SiC drivers. It features an asymmetric voltage output configuration to minimize SiC drive losses as much as possible. It also includes output short-circuit protection and self-recovery capabilities. This product is suitable for:

- General-purpose variable-frequency drives
- AC servo drive systems
- Electric welding machine
- Uninterruptible power supplies (UPS)

Product selection table

Ce rt if ic at io n	Model number	Input voltage range (Vdc)	Output Voltage/Current		Ripple and Noise	Efficiency@ Full load	Max. capaci tive load
		Rated value ® (Range)	Output Voltage (Vdc)	Output I (mA) (Max. Min.)	Full Load (mVp-p) Typ./Max .	%, (Min./Typ.)	μ F
	QA053C-1505R3	5 (4.5-5.5)	+15/-5	+80/-40	50/100	78/82	1000
	QA053C-2004R3		+20/-4	+80/-40			470
	QA053C-1803R3		+18/-3.5	+80/-80			680
	QA123C-1502R3	12 (10.8-13.2)	+15/-2.5	+100/-100	50/100	82/87	2200
	QA123C-1803R3		+18/-3				1000
	QA123C-1504R3		+15/-4	+120/-120			2200
	QA153C-1504R3	+15/-4	2200				
	QA153C-2005R3	15 (13.5-16.5)	+20/-5	+90/-90			

	QA123C-2005R3	12 (10.8-13.2)	+20/-5	+90/-90		470
	QA243C-1504R3	24 (21.6-26.4)	+15/-4	+120/-120	77/82	2200
	QA243C-2005R3		+20/-5	+90/-90	76/81	2200

Note: 1. Due to space limitations, the above is only a list of representative products. For products not included in this list, please contact our Sales Department.

2. The maximum capacitive load indicates the maximum capacitive load that can be connected to +Vo or -Vo. If this value is exceeded, the product will not start properly.

Test Conditions: Unless otherwise specified, all parameter measurements were taken at the nominal input voltage, with a purely resistive rated load, and at an ambient temperature of 25° C.

Input characteristics

Items		Working conditions	Min	Typ.	Max.	Unit
Input voltage	Vin=5VDC	DC	-0.7	—	9	VDC
	Vin=12VDC	DC	-0.7	—	18	
	Vin=15VDC	DC	-0.7	—	21	
	Vin=24VDC	DC	-0.7	—	30	
Input filter type		Capacitor filtering				
Hot swap		Not supported				

Output characteristics

Items		Working conditions	Min.	Typ.	Max.	Unit
QA053C-1505R3	+Vo	Vin=5VDC, Pin6 & Pin7 +Io= +80mA	14.55	15.3	16.05	VDC
	-Vo	Vin=5VDC, Pin5 & Pin6 -Io= -40mA	-4.45	-4.7	-4.95	
QA053C-2004R3	+Vo	Vin=5VDC, Pin6 & Pin7 +Io= +80mA	18.8	19.8	20.8	
	-Vo	Vin=5VDC, Pin5 & Pin6 -Io= -40mA	-3.8	-4	-4.2	
QA053C-1803R3	+Vo	Vin=5VDC, Pin6 & Pin7 +Io= +80mA	16.74	17.64	18.54	
	-Vo	Vin=5VDC, Pin5 & Pin6 -Io= -80mA	-3.13	-3.3	-3.67	
QA123C-1502R3	+Vo	Vin=12VDC, Pin6 & Pin7 +Io= +100mA	13.86	14.61	15.36	
	-Vo	Vin=12VDC, Pin5 & Pin6 -Io= -100mA	-2.28	-2.40	-2.53	
QA123C-1803R3	+Vo	Vin=12VDC, Pin6 & Pin7 +Io= +100mA	17.10	18.00	18.90	
	-Vo	Vin=12VDC, Pin5 & Pin6 -Io= -100mA	-3.00	-3.15	-3.30	
QA123C-1504R3	+Vo	Vin=12VDC, Pin6 & Pin7 +Io= +120mA	14.25	15.00	15.75	
	-Vo	Vin=12VDC, Pin5 & Pin6 -Io= -120mA	-3.60	-3.80	-4.00	
QA123C-2005R3	+Vo	Vin=12VDC, Pin6 & Pin7 +Io= +90mA	18.50	19.50	20.50	

		-Vo	Vin=12VDC, Pin5 & Pin6 -Io= -90mA	-4.95	-5.20	-5.45	
QA153C-1504R3		+Vo	Vin=15VDC, Pin6 & Pin7 +Io= +120mA	13.76	14.51	15.26	
		-Vo	Vin=15VDC, Pin5 & Pin6 -Io= -120mA	-3.80	-4.00	-4.20	
QA153C-2005R3		+Vo	Vin=15VDC, Pin6 & Pin7 +Io= +90mA	18.50	19.50	20.50	
		-Vo	Vin=15VDC, Pin5 & Pin6 -Io= -90mA	-4.95	-5.20	-5.45	
QA243C-1504R3		+Vo	Vin=24VDC, Pin6 & Pin7 +Io= +120mA	14.55	15.30	16.05	
		-Vo	Vin=24VDC, Pin5 & Pin6 -Io= -120mA	-3.96	-4.16	-4.36	
QA243C-2005R3		+Vo	Vin=24VDC, Pin6 & Pin7 +Io= +90mA	19.00	20.00	21.00	
		-Vo	Vin=24VDC, Pin5 & Pin6 -Io= -90mA	-4.75	-5.00	-5.25	
Output voltage accuracy			10% -100% load	See the error envelope curve (Figure 3- Figure 24)			
Line Regulation	(5V Model No.)	Within the full input voltage range	+ output	--	±1.1	±1.4	--
			- output	--	±1.1	±1.4	
	Other Model No.		+ output	--	±1.1	±1.5	
			- output	--	±1.1	±1.5	
Load Regulation	(5V Model No.)	10% -100% load	+ output	--	8	15	%
			- output	--	10	15	
	QA123C-1502R3		+ output	--	8	17	
			- output	--	13	17	
	其他型号		+ output	--	6	15	
			- output	--	8	15	
Temperature drift coefficient			Full load	--	±0.04	±0.1	%/°C
Ripple & Noise *			20MHz bandwidth	--	50	100	mVp-p
Output short-circuit protection			Sustainable, automatic recovery --				

Note: *The test method for ripple and noise uses the twisted-pair test method; see the Test Application Reference.

General characteristics

Items	Working conditions	Min.	Typ.	Max.	Unit
Insulation voltage	Input-output, test duration: 1 minute, leakage current less than 1 mA.	5000	--	--	VDC
Insulation resistance	Input-output, insulation voltage 500 VDC.	1000	--	--	MΩ
Isolation capacitor	Input-Output, 100 kHz/0.1 V	--	3.5	5	pF

Operating temperature	Derate for use when temperature $\geq 85^{\circ}\text{C}$ (see Figure 2)	-40	--	105	$^{\circ}\text{C}$
Storage temperature		-55	--	+125	
Outside cover temperature rise while operating	Ta=25 $^{\circ}\text{C}$	--	25	--	
Storage temperature	No condensation	5	--	95	%RH
Pin soldering temperature resistance	The solder joint is 1.5 mm from the housing; 10 seconds	--	--	+300	$^{\circ}\text{C}$
Switching frequency	Full load, rated voltage input	--	200	--	KHz
Vibration		10-150Hz, 5G, 0.75mm. along X, Y and Z			
Housing material		Black flame-retardant, heat-resistant plastic (UL94 V-0)			
Minimum mean time between failures	MIL-HDBK-217F@25 $^{\circ}\text{C}$	3.5X10 ⁶	--	--	Hrs

Product Performance Curve

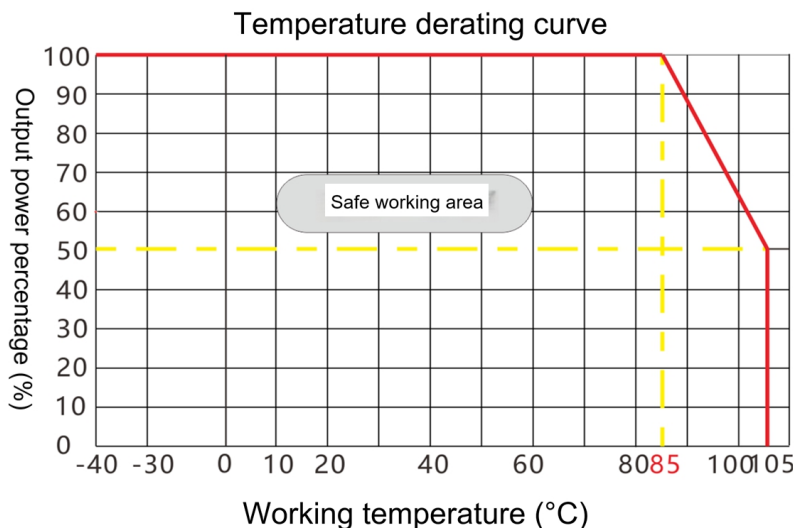


Figure 1 (Temperature derating curve)

QA053C-1505R3
主路误差包络曲线图

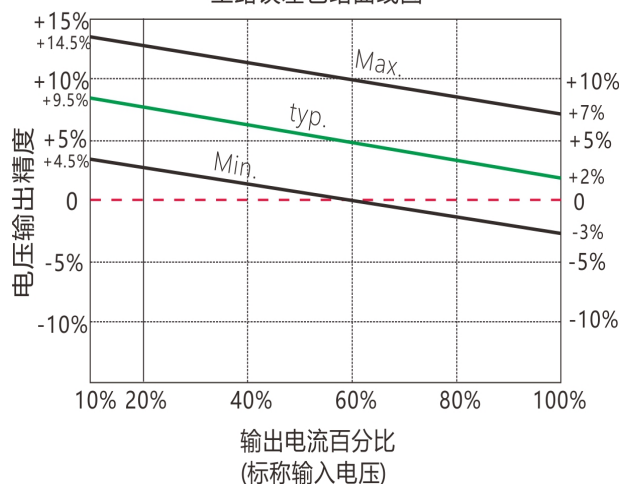
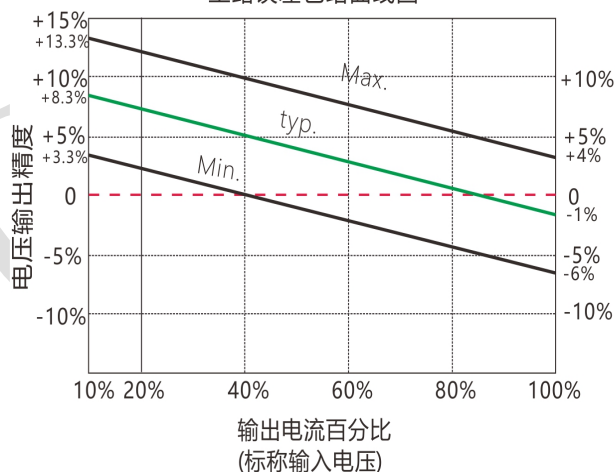


图 3 (主路误差包络曲线)
QA053C-2004R3
主路误差包络曲线图



QA053C-1505R3
辅路路误差包络曲线图

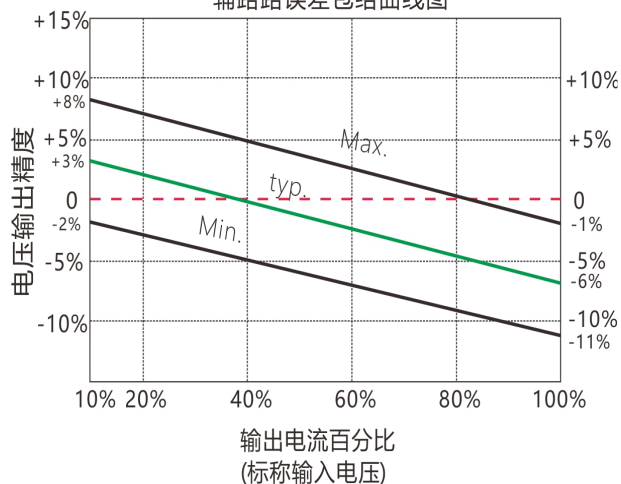
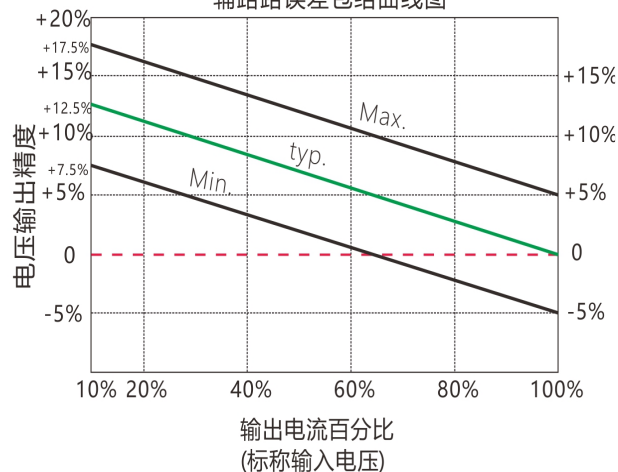
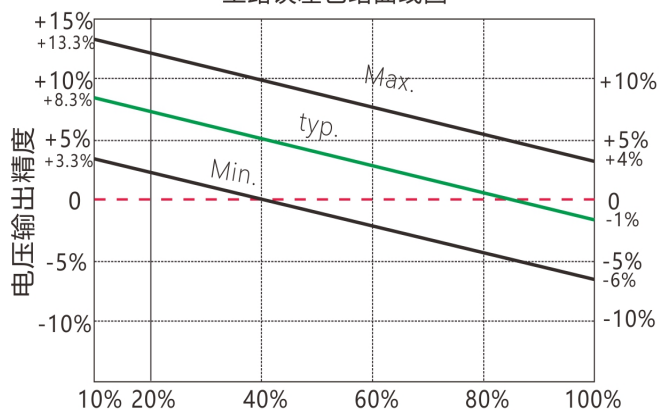


图 4 (辅路误差包络曲线)
QA053C-2004R3
辅路路误差包络曲线图



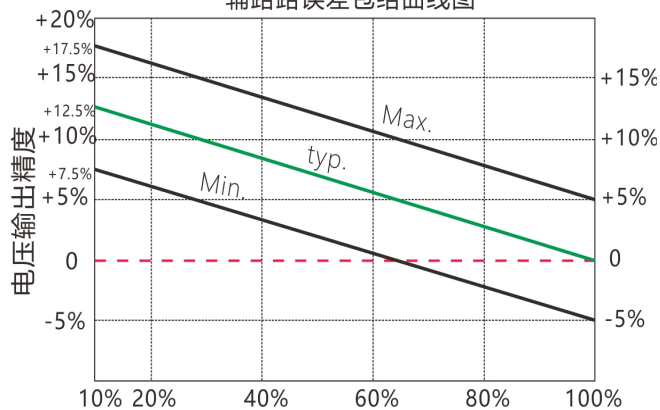
QA053C-2004R3

主路误差包络曲线图



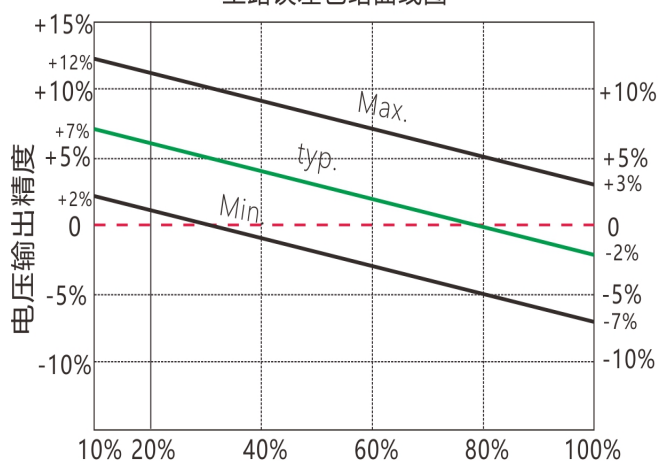
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辅路路误差包络曲线图



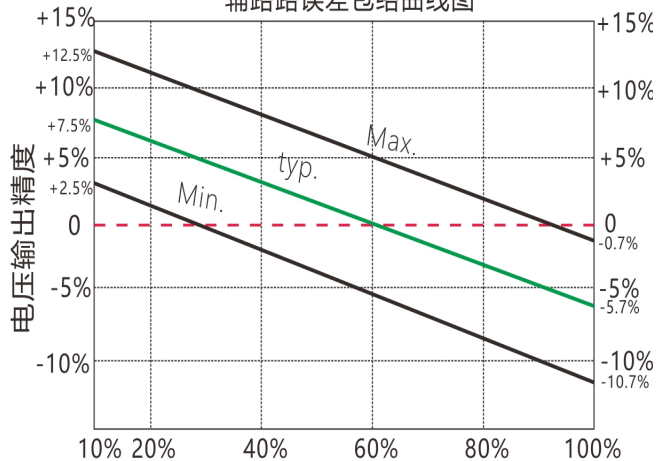
QA053C-1803R3

主路误差包络曲线图



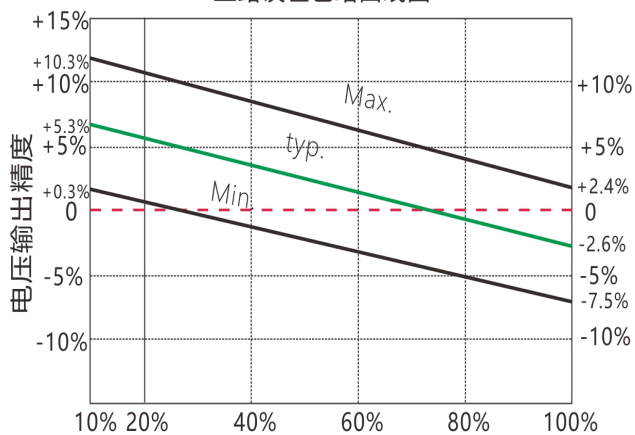
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辅路路误差包络曲线图



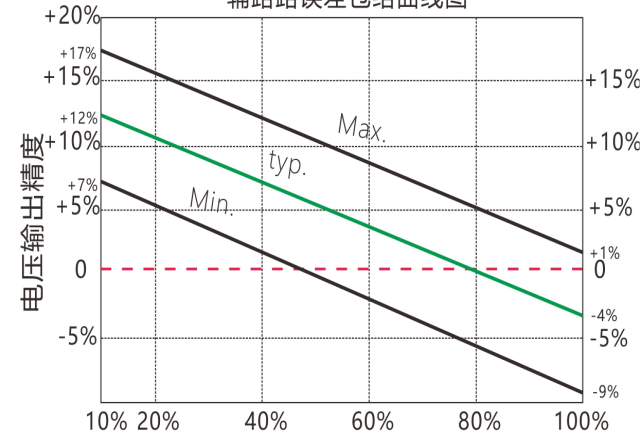
QA123C-1502R3

主路误差包络曲线图

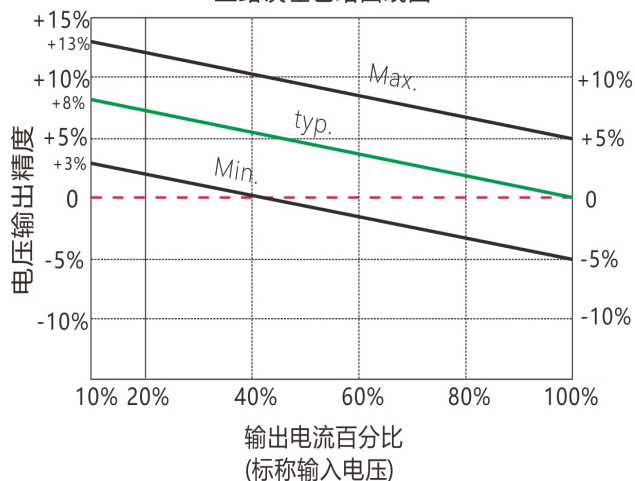


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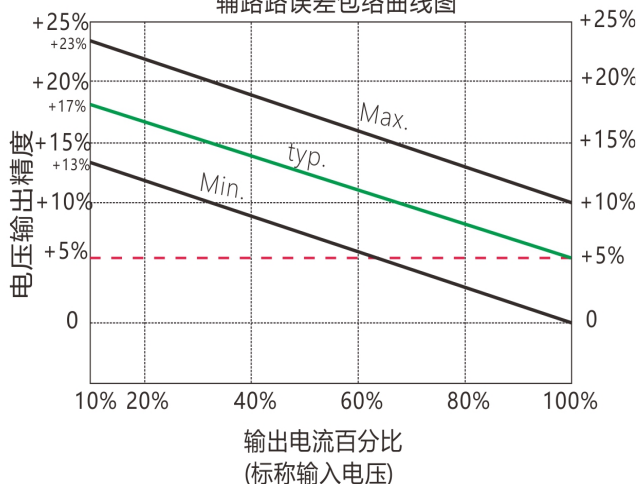
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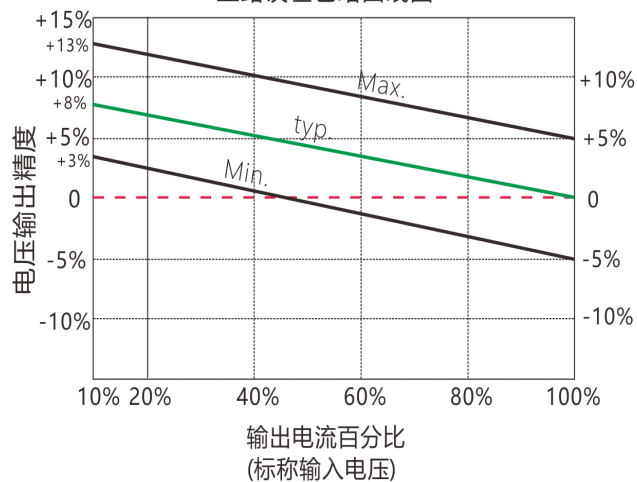
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主路误差包络曲线图



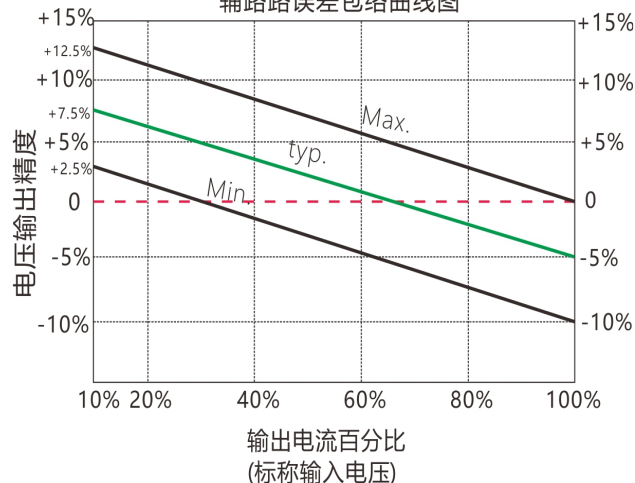
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辅路路误差包络曲线图



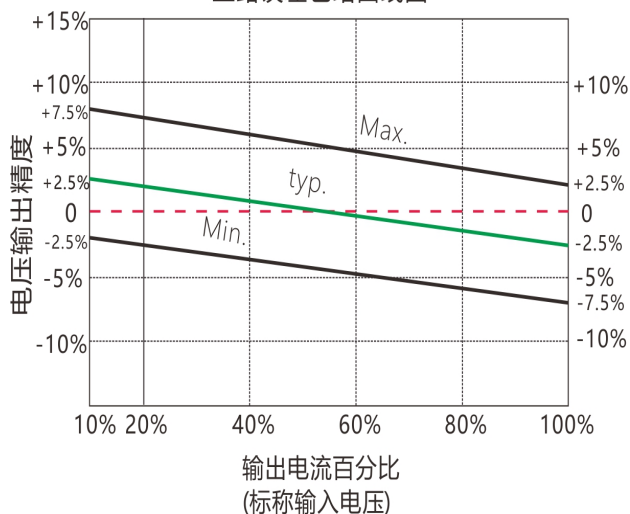
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主路误差包络曲线图



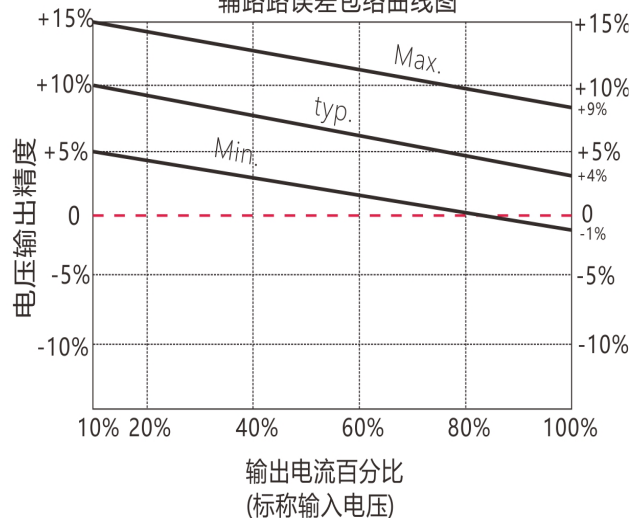
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辅路路误差包络曲线图



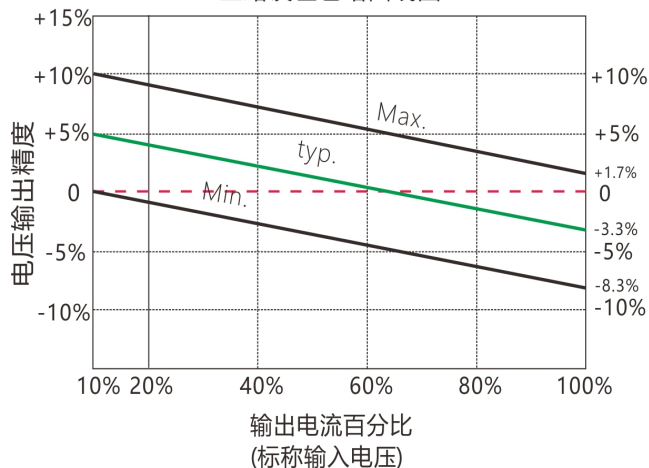
QA123C-2005R3
主路误差包络曲线图



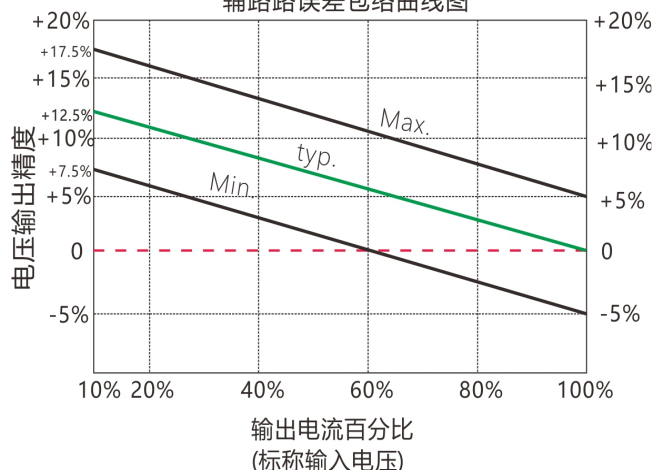
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辅路路误差包络曲线图



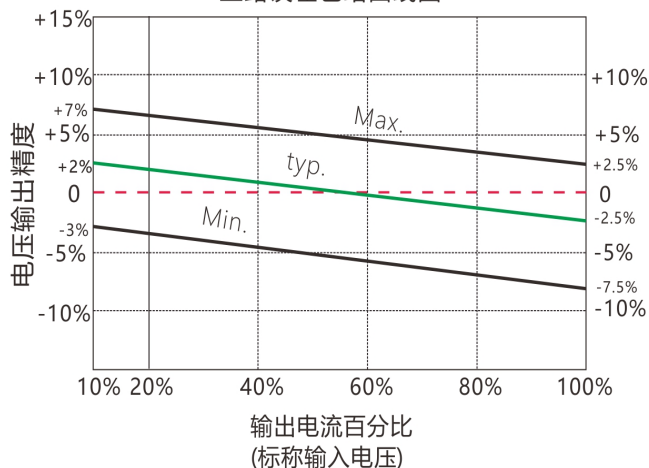
QA153C-1504R3
主路误差包络曲线图



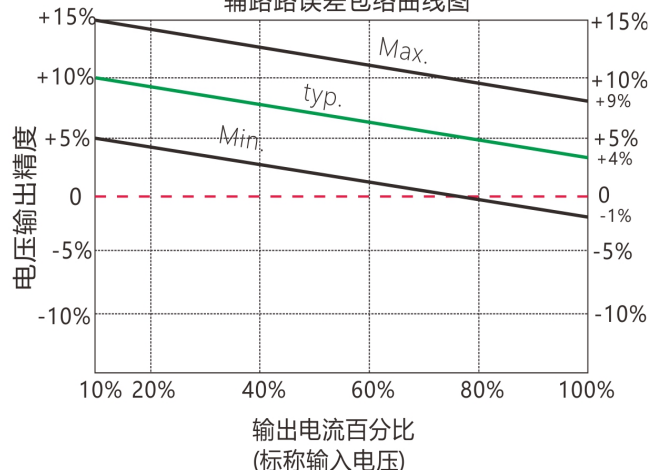
QA153C-1504R3
辅路路误差包络曲线图



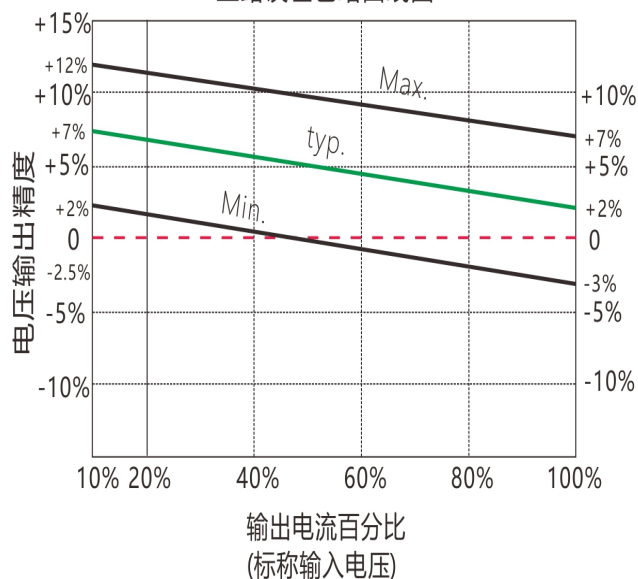
QA123C-2005R3
主路误差包络曲线图



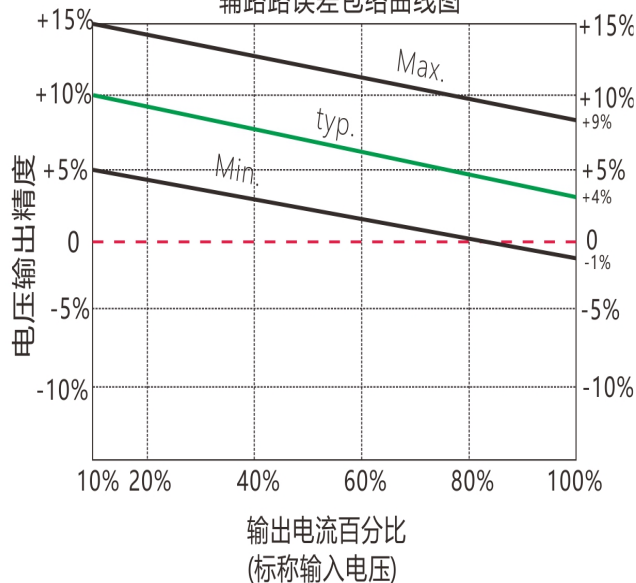
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辅路路误差包络曲线图

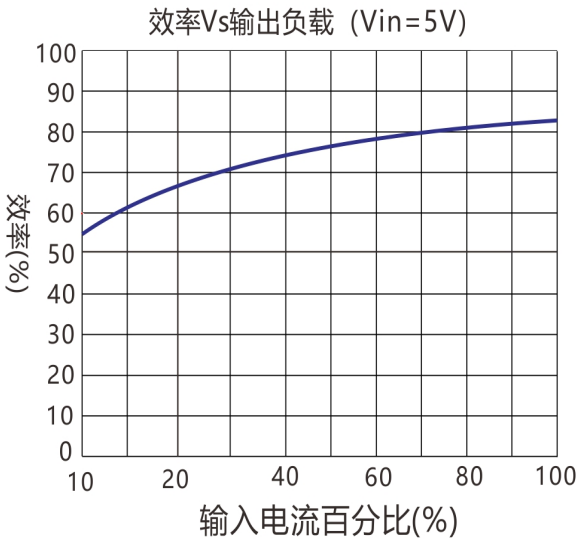
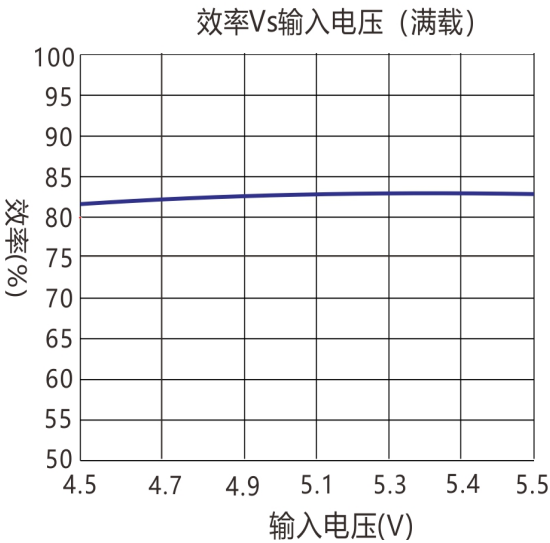
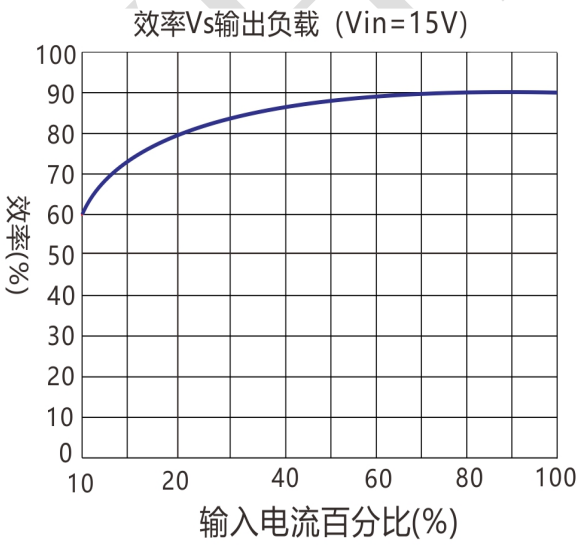
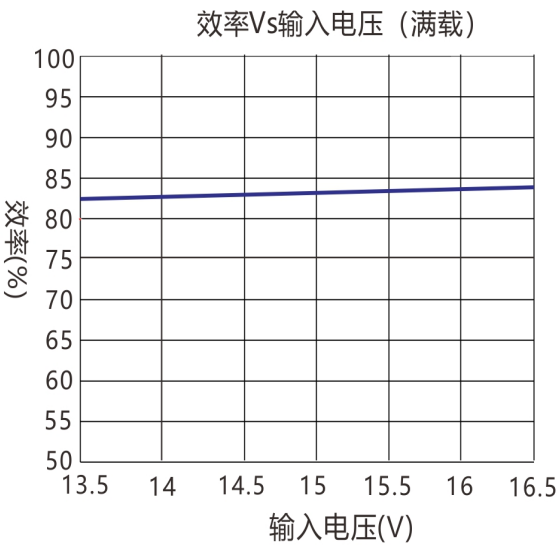
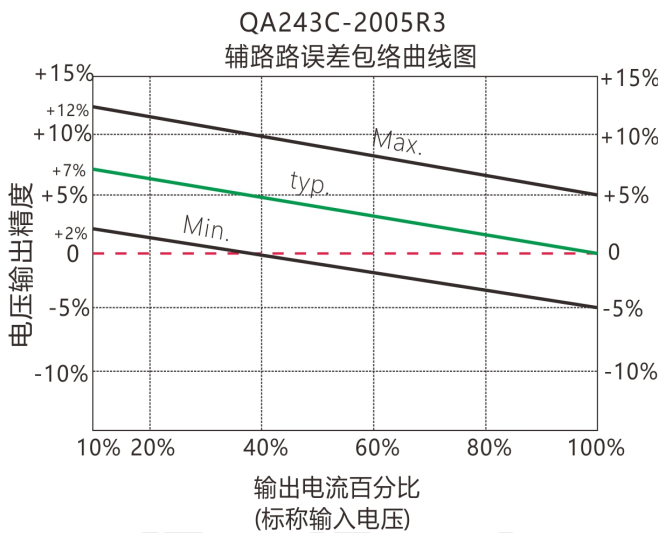
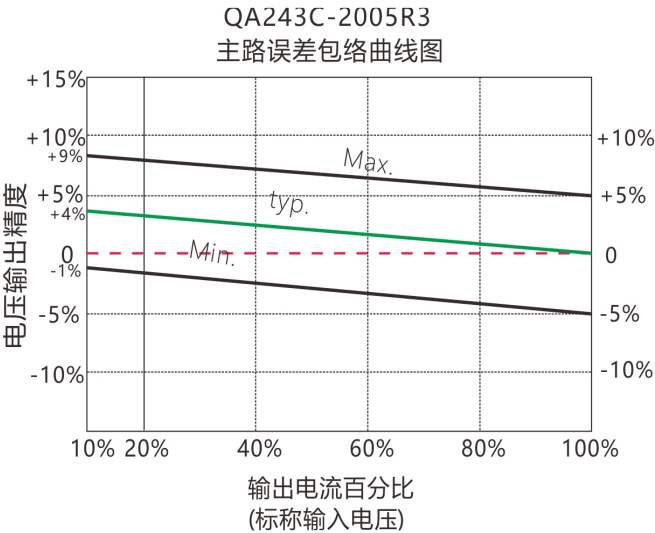


QA243C-1504R3
主路误差包络曲线图



QA243C-1504R3
辅路路误差包络曲线图





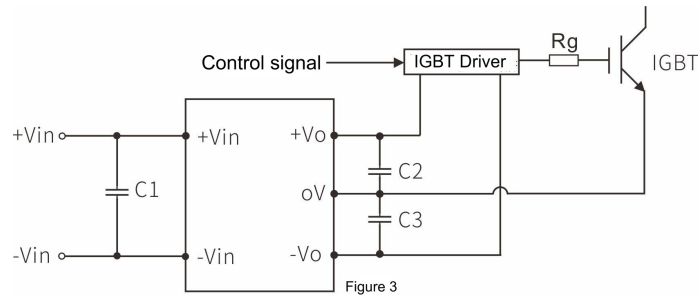
Typical Application Reference Circuit (Recommended Parameters)

1. General applications:

If further reduction of input and output ripple is required, a capacitor filter network can be connected across the input and output terminals; the circuit is shown in Figure 1. However, care must be taken to select an appropriate filter capacitor. If the capacitor is too large, it is likely to cause startup issues. For each output channel, the recommended capacitive load values—which ensure safe and reliable operation—are listed in Table 1.

Recommended capacitive load values (Table 1)

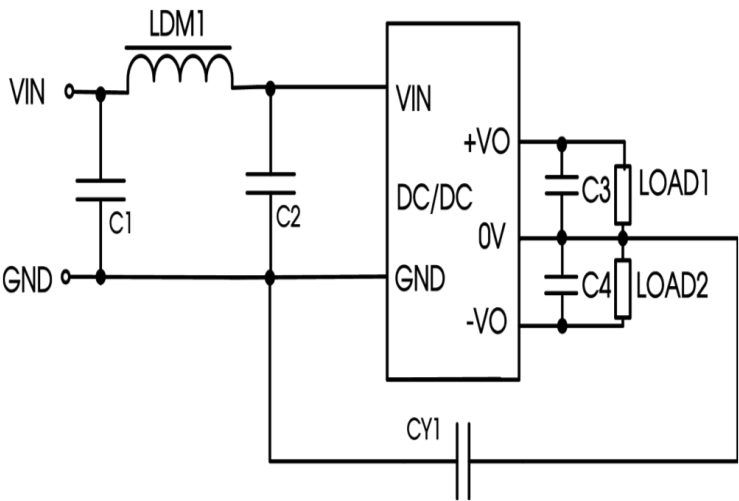
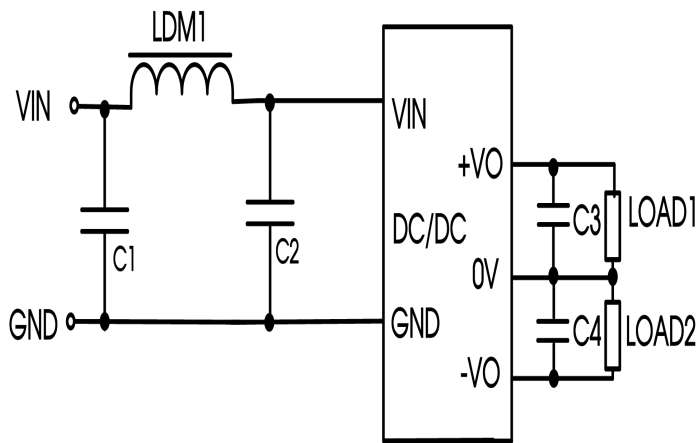
Vin	Cin	Vo	Cout
12VDC	2.2μF/25V	5VDC	4.7μF/16V
24VDC	1μF/50V	12VDC	1μF/16V
—	—	15VDC	1μF/25V



2. Typical EMI application circuit

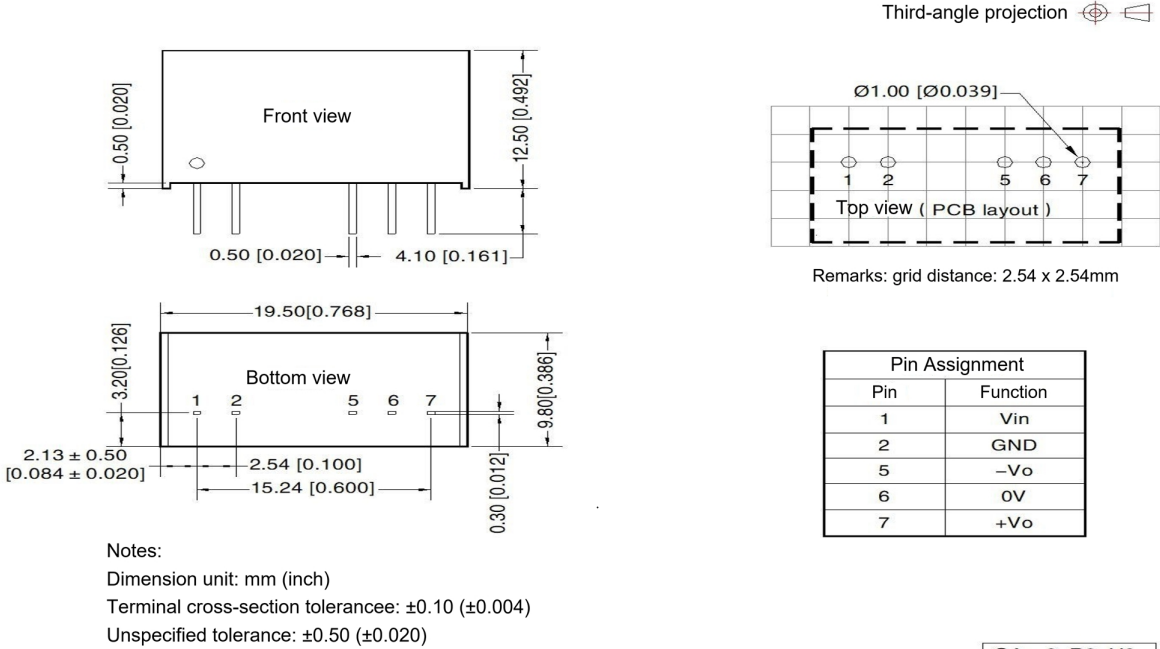
Recommended EMI reference circuit values (Table 2)

Items		5V Input model	Other model
EMI	C1/C2	4.7μF /16V	1μF/50V
	C3/C4	10μF /50V (Low-impedance capacitor)	100μF/30V (Low-impedance capacitor)
	LDM	6.8μH	33μH



EMI	C1/C2	4.7μF /16V
	C3/C4	10μF /50V (Low-impedance capacitor)
	LDM	6.8μH
	CY1	330pF

Appearance Dimensions and Pin Assignment, Recommended PCB Layout



QAx3-R3-V0

*Note: If the pin assignments of the power module differ from those in the selection guide, refer to the labels on the actual unit.

Package Description

Package code	L x W x H	
D	19.50 x 9.8x 12.50 mm	0.767 × 0.385 × 0.492inch

Test Application Reference

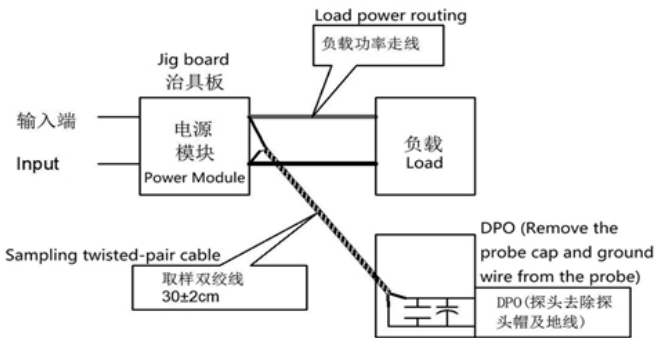
Ripple & Noise Testing: (Twisted-pair method, 20 MHz bandwidth)

Test Method:

1. For ripple and noise testing, use a Category 12 twisted-pair cable. Set the oscilloscope bandwidth to 20 MHz and use a 100 MHz bandwidth probe. Connect a 0.1 μ F polypropylene capacitor and a 4.7 μ F high-frequency, low-impedance electrolytic capacitor in parallel at the probe tip. Set the oscilloscope to “Sample” mode.

2. Schematic diagram for output ripple and noise testing:

Connect the power supply input to the power source. Connect the power supply output to the electronic load via the fixture board. For testing, use a 30 cm $\pm$ 2 cm sampling lead to take a direct sample from the power supply output port. Select insulated wires with appropriate gauge based on the magnitude of the output current.



Application Precautions

1. Input Requirements: Ensure that the output voltage fluctuation range of the power supply does not exceed the input requirements of the DC/DC module itself; the output power of the input power supply must be greater than the output power of the DC/DC module.
2. Recommended Circuit 1: For applications with general ripple and noise requirements, a filter capacitor can be connected in parallel at both the input and output terminals. The external circuit is shown in Figure (1) below; refer to Table (1). Output Load Requirements: Avoid operating under no-load conditions whenever possible. When the actual power consumption of the load is less than 10% of the module's rated output power or no-load conditions occur, it is recommended to connect a dummy load to the output. The dummy load (resistor) can be calculated based on 5 - 10% of the module's rated power, where the resistance value = $U_{out} / (1W \times R3 \times 10\%)$;
3. Overload Protection: Under normal operating conditions, the product's output circuit does not provide overload protection. Prolonged overload will trigger over-temperature protection, causing the output to shut down;
4. The output features continuous short-circuit protection with automatic recovery.
5. The capacitance of the external capacitor connected to the output should not be too large, as this may cause overcurrent or poor startup during module startup;
6. If the product operates below the minimum required load, performance compliance with all specifications in this manual cannot be guaranteed;
7. Maximum capacitive loads are tested within the input voltage range and under full-load conditions;
8. Unless otherwise specified, all specifications in this manual are measured at $T_a=25^\circ\text{C}$, humidity <75% RH, nominal input voltage, and rated output load;
9. All test methods for the specifications in this manual are based on our company's standards;
10. We offer product customization; please contact our technical staff directly for details;
11. Product specifications are subject to change without notice.

Contact

GDHUIZHI®

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Tel: +86 - 758 - 2566 585

E-mail: sales@huizhi-elec.com