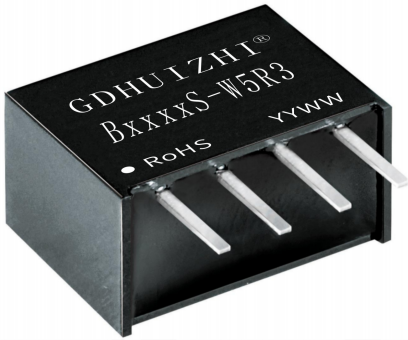


Typical Characteristics

- Fixed-voltage input, isolated unregulated output, Power: 0.5W
- Isolation voltage: 1500VDC/1min, 3000VDC/1s
- Low no-load power consumption: 0.025W(Typ.)
- Working ambient temperature: -40℃~+85℃
- Efficiency: up to 84%
- Output short-circuit protection: continuous short-circuit protection, automatic recovery
- Industry standard pin-out
- Compact SIP package
- Ripple/noise (20MHz bandwidth): 30mVp-p(typ.)
- MTBF≥3.5 million hours (3500000Hrs)

0.5W, fixed-voltage input, isolated unregulated, single-output DC/DC power module



Over-temperature protection and continuous output short-circuit protection
RoHS

B_S-W5R3 Series—is a compact, high-efficiency, low-power, fixed-input-voltage, isolated, non-regulated single-output DC/DC module power supply designed by Huizhi Electronics for its customers.
This product series is specifically designed for applications in on-board power supply systems that require the generation of a voltage isolated from the input power source.
This product is suitable for:

- The input voltage is relatively stable (voltage variation range: $\pm 10\%$ Vin) ;
- Isolation is required between input and output (isolation voltage ≤ 1500 VDC);
- The output voltage stability requirements are not high;
- Typical applications: Pure digital circuits, general-purpose low-frequency analog circuits, relay driver circuits, data switching circuits, etc.

Model Number Description

Cer tif ica tio n	Model number	Input voltage range (Vdc)	Output Voltage/Current		Ripple and Noise	Efficiency@ Full load	Max. capaci tive load
		Rated value ^② (Range)	Output V (Vdc)	Output I (mA) (Max. Min.)	Full Load (mVp-p) Typ. /Max.	%, (Min. /Typ.)	μ F
	B0303S-W5R3	3.3 (2.97-3.63)	3.3	151/15	30/80	70/74	2400
	B0305S-W5R3		5	100/10	30/80	78/82	2400
	B0312S-W5R3		12	42/4	50/120	78/82	560

	B0503S-W5R3	5 (4.5-5.5)	3.3	151/15	30/80	70/74	2400
	B0505S-W5R3		5	100/10	30/80	78/82	2400
	B0509S-W5R3		9	55/5	30/80	79/83	1000
	B0512S-W5R3		12	42/4	50/100	79/83	560
	B0515S-W5R3		15	33/3	50/100	79/83	560
	B0524S-W5R3		24	21/2	50/150	81/85	220
	B0903S-W5R3	9 (8.1-9.9)	3.3	151/15	30/80	71/75	2400
	B0905S-W5R3		5	100/10	30/80	76/80	2400
	B0909S-W5R3		9	55/5	30/80	76/80	1000
	B0912S-W5R3		12	42/4	30/80	76/80	560
	B0915S-W5R3		15	33/3	30/80	77/81	560
	B0924S-W5R3		24	21/2	50/100	77/81	220
	B1203S-W5R3	12 (10.8-13.2)	3.3	151/15	30/80	71/75	2400
	B1205S-W5R3		5	100/10	30/80	76/80	2400
	B1209S-W5R3		9	55/5	30/80	76/80	1000
	B1212S-W5R3		12	42/4	30/80	76/80	560
	B1215S-W5R3		15	33/3	30/80	77/81	560
	B1224S-W5R3		24	21/2	50/100	77/81	220
	B1503S-W5R3	15 (13.5-16.5)	3.3	151/15	30/80	76/80	2400
	B1505S-W5R3		5	100/10	30/80	76/80	2400
	B1509S-W5R3		9	55/5	30/80	76/80	1000
	B1512S-W5R3		12	42/4	30/80	76/80	560
	B1515S-W5R3		15	33/3	30/80	77/81	560
	B1524S-W5R3		24	21/2	50/100	77/81	220
	B2403S-W5R3	24 (21.6-26.4)	3.3	151/15	30/80	69/75	2400
	B2405S-W5R3		5	100/10	30/80	73/79	2400
	B2409S-W5R3		9	55/5	30/80	74/80	1000
	B2412S-W5R3		12	42/4	30/80	75/81	560
	B2415S-W5R3		15	33/3	30/80	75/81	560
	B2424S-W5R3		24	21/2	50/100	75/81	220

Note: 1. Due to space limitations, the above is only a list of representative products. For products not included in this list, please contact our Sales Department.

2. The maximum capacitive load indicates the maximum capacitive load that can be connected to +Vo or -Vo. If this value is exceeded, the product will not start properly.

Test Conditions: Unless otherwise specified, all parameter measurements were taken at the rated input voltage, with a purely resistive rated load, and at an ambient temperature of 25° C.

Input characteristics

Items	Working conditions	Min.	Typ.	Max.	Unit
Input Current (Full Load/No Load)	3.3VDC input series	--	271/15	286/-	mA
	5VDC input series	--	244/15	257/-	
	9VDC input series	--	142/8	146/--	
	12VDC input series	--	105/8	110/--	

	15VDC input series	--	84/8	88/--	
	24VDC input series	--	53/8	58/--	
Reflected Ripple Current		--	15	--	
Impulse voltage (I sec. max)	3.3VDC input series	-0.7	--	5	VDC
	5VDC input series	-0.7	--	9	
	9V,12VDC input series	-0.7	--	18	
	15VDC input series	-0.7	--	21	
	24VDC input series	-0.7	--	30	
Input filter type		Capacitor filtering			
Hot swap		Not supported			

Output characteristics						
Items	Working and test conditions		Min.	Typ.	Max.	Unit
Output load	Load percentage		10	--	100	%
Output voltage accuracy	See the error envelope curve		--	--	±15.0	%
Line Regulation	Input voltage variation: ±1%	3.3V output	--	--	±1.5	%
		Others	--	--	±1.2	%
Load Regulation	10%~100% load	3.3VDC output	--	15	20	%
		5VDC output	--	10	15	%
		9VDC output	--	8	10	%
		12VDC output	--	7	10	%
		15VDC output	--	6	10	%
		24VDC output	--	5	10	%
Ripple & Noise	Pure resistive load, 20 MHz bandwidth, peak-to-peak value	Other output	--	30	80	mVp-p
		24VDC output		50	100	
Temperature drift coefficient	Full load		--	±0.02	--	%/°C
Output short-circuit protection	Sustainable short-circuit protection, automatic recovery					

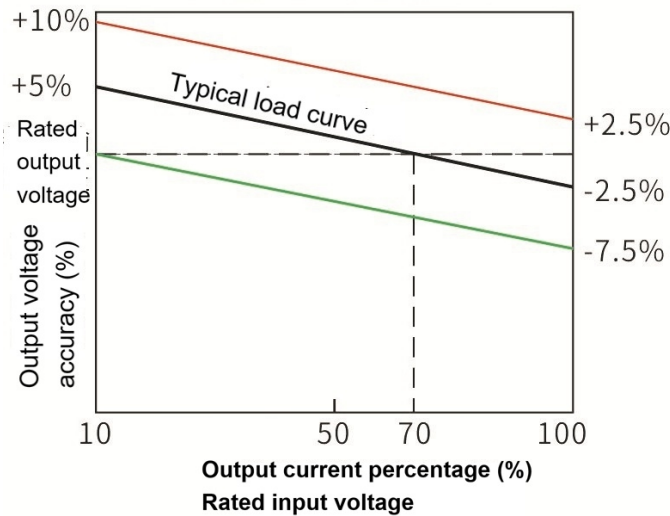
Note: ① Test method for ripple and noise: twisted-pair test method.

General characteristics						
Items	Working conditions		Min.	Typ.	Max.	Unit
Insulation voltage	Input-output, test duration: 1 minute, leakage current less than 1 mA.		1500	--	--	VDC
Insulation resistance	Input-output, insulation voltage 500 VDC.		1000	--	--	MΩ
Isolation capacitor	Input-Output, 100 kHz/0.1 V		--	20	--	pF
Operating temperature	Use the reference temperature derating curve		-40	--	+85	°C

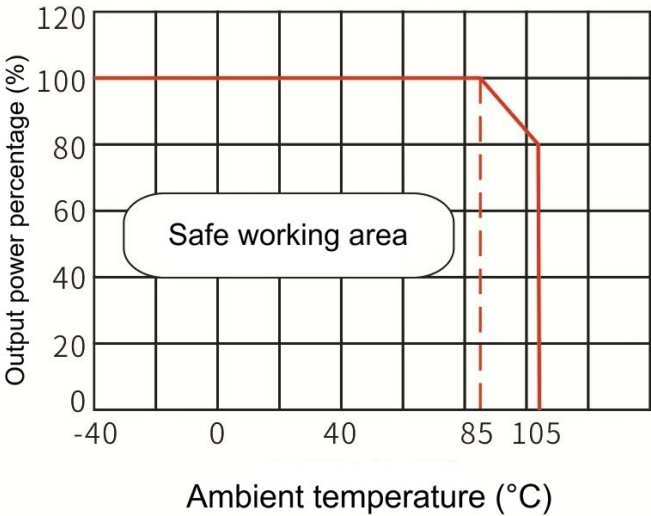
Storage temperature		-40	--	+125	
Maximum operating case temperature		--	25	--	
Storage humidity	No condensation	5	--	95	%RH
Pin soldering temperature resistance	The solder joint is 1.5 mm from the housing; 10 seconds	--	--	+300	℃
Switching frequency	Full load, rated voltage input	--	100	--	KHz
Vibration		10-55Hz, 10G, 30 Min. along X, Y and Z			
Housing material		Black flame-retardant, heat-resistant plastic (UL94 V-0)			
Minimum mean time between failures	MIL-HDBK-217F@25℃	3.5X10 ⁶	--	--	Hrs

Product Performance Curve

Error Envelope Curve



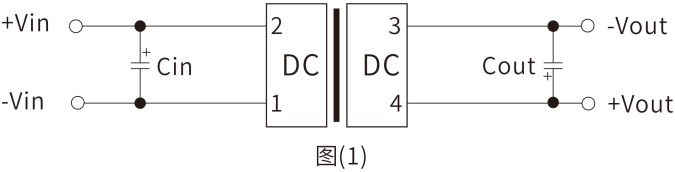
Temperature derating curve



Typical Application Reference Circuit (Recommended Parameters)

1. General applications:

Recommended capacitive load values (Table 1)



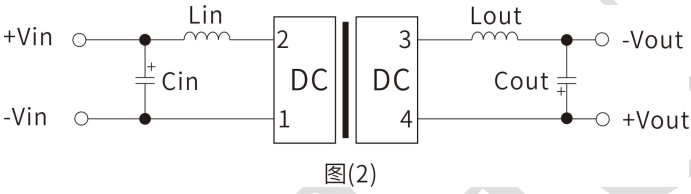
If further reduction of input and output ripple is required, a capacitor filter network can be connected across the input and output terminals; the circuit is shown in Figure 1.

Vin (Vdc)	Cin(u	Vo (Vdc)	Cout(u F)
3.3/5	4.7	3.3/5	10
12	2.2	9	4.7
15	2.2	12	2.2
24	1	15	1
-	-	24	1

However, care must be taken to select an appropriate filter capacitor. If the capacitor is too large, it is likely to cause startup issues. For each output channel, the recommended capacitive load values—which ensure safe and reliable operation—are listed in Table 1.

2. Typical EMI application circuits

Recommended EMI reference circuit values (Table 2)



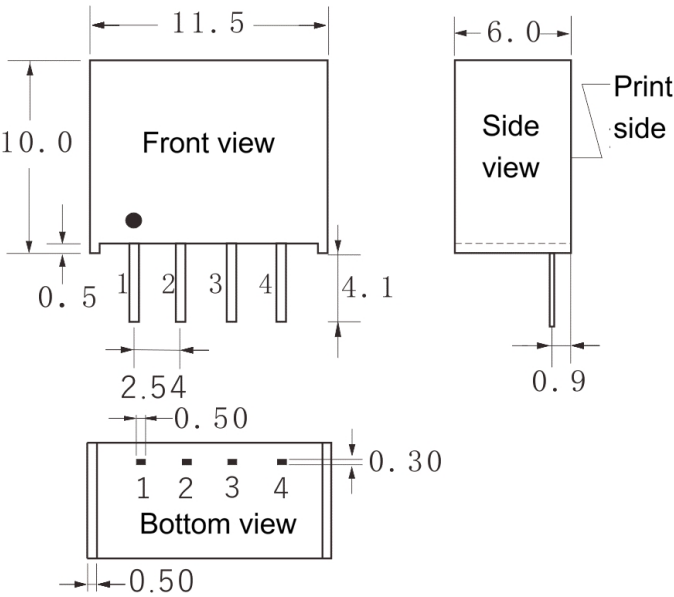
Vin (Vdc)	3.3/5/12/15/24
Cin	4.7u F/50V
Cout	参考表1
Lin	4.7uH
Lout	4.7uH

3. Output load requirements

To ensure that this module operates efficiently and reliably, the minimum output load must not be less than 10% of the rated load during use. If your power requirement is indeed very low, please connect a resistor in parallel between the positive and negative terminals of the output (the total power rating of the resistor must be greater than or equal to 10% of the rated power, and the selected resistor's power rating must be at least 5 times greater than the actual power consumption; otherwise, the resistor will become excessively hot).

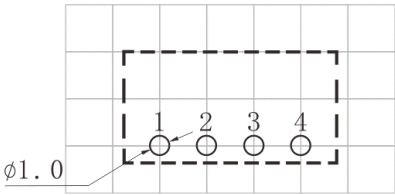
Appearance Dimensions and Pin Descriptions, Recommended PCB Layout

1) Appearance Dimensions (Unit: mm, Tol.: xx ±0.25) 2) Pin Assignment



1	2	3	4
-Vin	+Vin	-Vout	+Vout
- input	+ input	- output	+ output

3) Recommended layout



Remarks: grid distance : 2.54*2.54mm

*Note: If the pin assignments of the power module differ from those in the selection guide, refer to the labels on the actual unit.

Package Description

Package code	L x W x H	
S	11.50 x 6.0 x 10.0mm	0.453 x 0.236 x 0.394inch

Test Application Reference

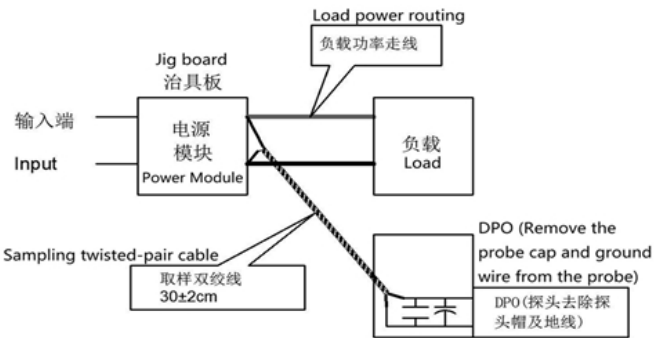
Ripple & Noise Testing: (Twisted-pair method, 20 MHz bandwidth)

Test Method:

1. For ripple and noise testing, use a Category 12 twisted-pair cable. Set the oscilloscope bandwidth to 20 MHz and use a 100 MHz bandwidth probe. Connect a 0.1 μ F polypropylene capacitor and a 4.7 μ F high-frequency, low-impedance electrolytic capacitor in parallel at the probe tip. Set the oscilloscope to “Sample” mode.

2. Schematic diagram for output ripple and noise testing:

Connect the power supply input to the input power source. Connect the power supply output to the electronic load via the fixture board. For testing, use a 30 cm $\pm$ 2 cm sampling lead to take a direct sample from the power supply output port. Select insulated wires with appropriate gauge based on the magnitude of the output current.



Application Precautions

1. Input Requirements: Ensure that the output voltage fluctuation range of the power supply does not exceed the input requirements of the DC/DC module itself; the output power of the input power supply must be greater than the output power of the DC/DC module.
2. Recommended Circuit 1: For applications with general ripple and noise requirements, a filter capacitor can be connected in parallel at both the input and output terminals. The external circuit is shown in Figure (1) below; refer to Table (1). Output Load Requirements: Avoid operating under no-load conditions whenever possible. When the actual power consumption of the load is less than 10% of the module's rated output power or no-load conditions occur, it is recommended to connect a dummy load to the output. The dummy load (resistor) can be calculated based on 5 - 10% of the module's rated power, where the resistance value = $U_{out} / (W5R3 * 10\%)$;
3. Overload Protection: Under normal operating conditions, the product's output circuit does not provide overload protection. Prolonged overload will trigger over-temperature protection, causing the output to shut down;
4. The output features continuous short-circuit protection with automatic recovery.
5. The capacitance of any external capacitor connected to the output should not be too large, as this may cause overcurrent or poor startup during module startup;
6. If the product operates below the minimum required load, performance compliance with all specifications in this manual cannot be guaranteed;
7. Maximum capacitive loads are tested within the input voltage range and under full-load conditions;
8. Unless otherwise specified, all specifications in this manual are measured at $T_a = 25^\circ \text{C}$, humidity $< 75\% \text{ RH}$, nominal input voltage, and rated output load;
9. All test methods for the specifications in this manual are based on our company's standards;
10. We offer product customization; please contact our technical staff directly for details;
11. Product specifications are subject to change without notice.

Contact

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