

2W, fixed-voltage input, isolated unregulated, dual-output (positive and negative), DC/DC power module

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Over-temperature protection and continuous
output short-circuit protection
RoHS

This product series is specifically designed for applications in board-level power supply systems that require the generation of a voltage isolated from the input power supply. This product is suitable for:

- Applications where the input power supply voltage is relatively stable (voltage variation range: $\pm 10\%$ Vin);
- Applications requiring isolation between input and output (isolation voltage ≤ 3000 VDC);
- Applications with low requirements for output voltage stability;
- Typical applications: Pure digital circuits, general-purpose low-frequency analog circuits, relay driver circuits, data switching circuits, etc.

E xx xx S - 2 W - R3

Suffix (3rd-generation product)

Rated output power (2W)

Package type (Long Single-In-line Package, SIP)

Output voltage (rated)

Input voltage (rated)

Product series (3000V isolated dual output)

Cer tif ica tio n	Model number	Input voltage range (Vdc)	Output Voltage/Current		Ripple and Noise	Efficiency@ Full load	Max. capaci tive load
		Rated value ^② (Range)	Output V (Vdc)	Output I (mA) (Max. Min.)	Full Load (mVp-p) Typ. /Max.	% (Min. / Typ.)	μ F

	E0503S-2WR3	5 (4.5-5.5)	±3.3	±303/±30	75/180	71/75	1200
	E0505S-2WR3		±5	±200/±20	75/180	80/84	1200
	E0509S-2WR3		±9	±111/±11	75/180	81/85	470
	E0512S-2WR3		±12	±83/±8	75/180	81/85	220
	E0515S-2WR3		±15	±67/±7	75/180	82/86	220
	E0524S-2WR3		±24	±42/±4	75/180	82/86	100
	E0903S-2WR3	9 (8.1-9.9)	±3.3	±303/±15	75/180	76/80	1200
	E0905S-2WR3		±5	±200/±10	75/180	86/88	1200
	E0909S-2WR3		±9	±112/±6	75/180	87/89	560
	E0912S-2WR3		±12	±84/±5	75/180	87/89	330
	E0915S-2WR3		±15	±67/±4	75/180	87/90	330
	E0924S-2WR3		±24	±42/±3	75/180	87/90	100
	E1203S-2WR3	12 (10.8-13.2)	±3.3	±303/±15	75/180	71/75	1200
	E1205S-2WR3		±5	±200/±10	75/180	76/80	1200
	E1209S-2WR3		±9	±112/±6	75/180	78/82	470
	E1212S-2WR3		±12	±84/±5	75/180	79/83	220
	E1215S-2WR3		±15	±67/±4	75/180	79/83	220
	E1224S-2WR3		±24	±42/±3	75/180	79/83	100
	E1503S-2WR3	15 (13.5-16.5)	±3.3	±303/±15	75/180	76/80	1200
	E1505S-2WR3		±5	±200/±10	75/180	86/88	1200
	E1509S-2WR3		±9	±112/±6	75/180	87/89	560
	E1512S-2WR3		±12	±84/±5	75/180	87/89	330
	E1515S-2WR3		±15	±67/±4	75/180	87/89	330
	E1524S-2WR3		±24	±42/±3	75/180	87/89	100
	E2403S-2WR3	24V (21.6~26.4)	±3.3	±303/±15	75/180	70/76	1200
	E2405S-2WR3		±5	±200/±10	75/180	74/80	1200
	E2409S-2WR3		±9	±112/±6	75/180	75/81	470
	E2412S-2WR3		±12	±84/±5	75/180	77/83	220
	E2415S-2WR3		±15	±67/±4	75/180	77/83	220
	E2424S-2WR3		±24	±42/±3	75/180	77/83	100

Note: 1. Due to space limitations, the above is only a list of representative products. For products not included in this list, please contact our Sales Department.

2. The maximum capacitive load indicates the maximum capacitive load that can be connected to +Vo or -Vo. If this value is exceeded, the product will not start properly.

Test Conditions: Unless otherwise specified, all parameter measurements were taken at the nominal input voltage, with a purely resistive rated load, and at an ambient temperature of 25° C.

Input characteristics

Items	Working conditions		Min.	Typ.	Max.	Unit
Input Current (Full Load/No Load)	5VDC input series	3.3VDC output	--	534/8	564/--	mA
		5VDC/7.2VDC output	--	477/8	500/--	
		9VDC/12VDC output	--	471/8	494/--	

	15VDC/24VDC output		--	466/8	488/--	
	9VDC input series		--	325/8	305/--	
	12VDC input series	3.3VDC output	--	222/8	235/--	
		5VDC/7.2VDC output	--	208/8	219/--	
		9VDC output	--	203/8	214/--	
		12VDC/15VDC/24VDC output	--	201/8	211/--	
	15VDC input series	5VDC/9VDC output	--	167/8	176/--	
		12VDC/15VDC/24VDC output	--	165/8	173/--	
	24VDC input series	3.3VDC output	--	110/8	119/--	
		9VDC output	--	103/8	111/--	
		12VDC output	--	99/8	107/--	
		15VDC/24VDC output	--	97/8	104/--	
	Reflected ripple current			--	15	
Impulse voltage (I sec. max)	5VDC input series		-0.7	--	9	VDC
	9VDC input series		-0.7	--	18	
	12VDC input series		-0.7	--	15	
	15VDC input series		-0.7	--	21	
	24VDC input series		-0.7	--	30	
Input filter type			Capacitor filtering			
Hot swap			Not supported			
Output characteristics						
Items	Working and test conditions		Min.	Typ.	Max.	Unit
Output load	Load percentage		10	--	100	%
Output voltage accuracy	See the error envelope curve		--	--	±15.0	%
Line Regulation	Input voltage variation: ±1%	3.3V output	--	--	±1.5	%
		Others	--	--	±1.2	%
Load Regulation	10%~100% load	3.3VDC output	--	18	--	%
		5VDC output	--	12	--	%
		9VDC output	--	8	--	%
		12VDC output	--	7	--	%
		15VDC output	--	6	--	%
		24VDC output	--	5	--	%
		Ripple & Noise	Pure resistive load, 20 MHz bandwidth, peak-to-peak value		--	75

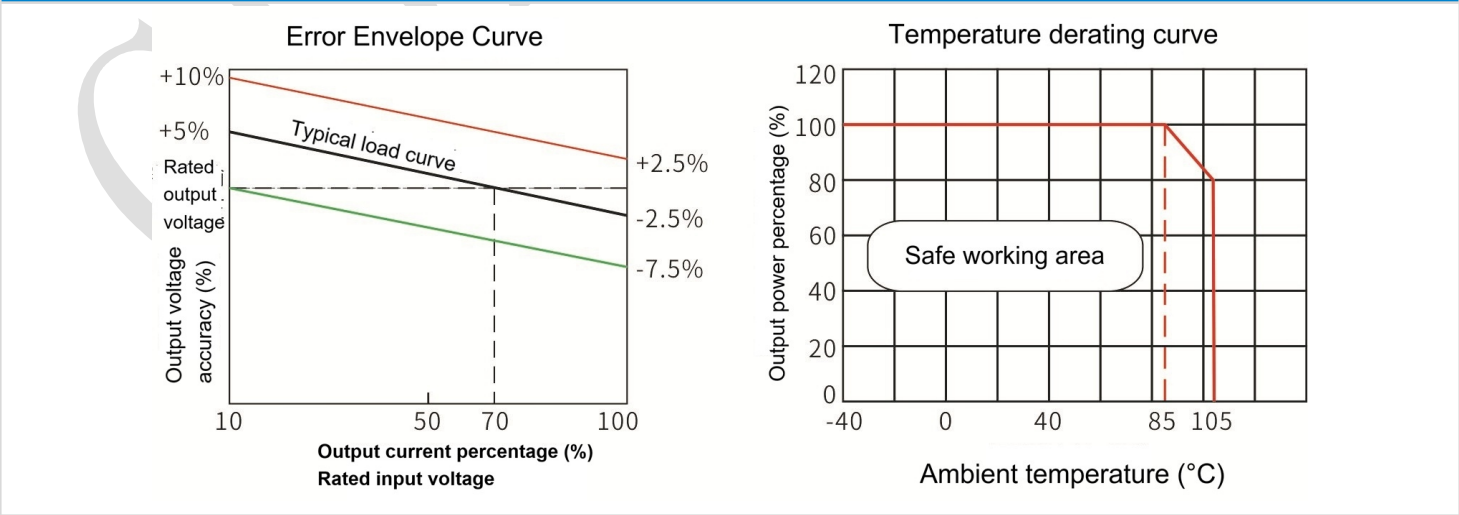
Temperature drift coefficient	Full load	--	--	±0.03	%/° C
Output short-circuit protection	Sustainable short-circuit protection, automatic recovery				

Note: ① Test method for ripple and noise: twisted-pair test method.

General characteristics

Items	Working conditions	Min.	Typ.	Max.	Unit
Insulation voltage	Input-output, test duration: 1 minute, leakage current less than 1 mA.	3000	--	--	VDC
Insulation resistance	Input-output, insulation voltage 500 VDC.	1000	--	--	MΩ
Isolation capacitor	Input-Output, 100 kHz/0.1 V	--	20	--	pF
Operating temperature	Use the reference temperature derating curve	-40	--	+85	℃
Storage temperature		-40	--	+125	
Maximum operating case temperature		--	25	--	
Storage humidity	No condensation	5	--	95	%RH
Pin soldering temperature resistance	The solder joint is 1.5 mm from the housing; 10 seconds	--	--	+300	℃
Switching frequency	Full load, rated voltage input	--	100	--	KHz
Vibration		10-55Hz, 10G, 30 Min. along X, Y and Z			
Housing material	Aluminum housing	-			
Minimum mean time between failures	MIL-HDBK-217F@25℃	3. 5X10 ⁶	--	--	Hrs

Product Performance Curve



Typical Application Reference Circuit (Recommended Parameters)

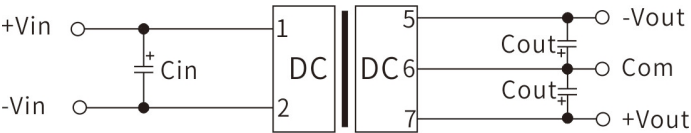
1. General applications:

If further reduction of input and output ripple is required, a capacitor filter network can be connected across the input and output terminals; the circuit is shown in Figure 1.

However, care must be taken to select an appropriate filter capacitor. If the capacitor is too large, it is likely to cause startup issues. For each output channel, the recommended capacitive load values—which ensure safe and reliable operation—are listed in Table 1.

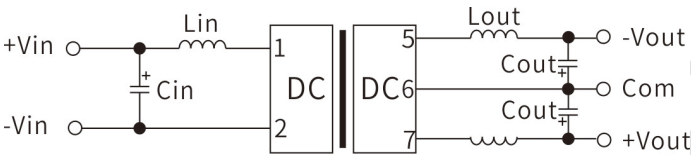
Recommended capacitive load values (Table 1)

Vin (Vdc)	Cin (u F)	Vo (Vdc)	Cout (u F)
3.3/5	4.7	3.3/5	10
9/12	2.2	12	2.2
15/24	1	15	1
—	—	24	0.47
—	—	—	—



图(1)

2. Typical EMI application circuits



图(2)

Recommended EMI reference circuit values (Table 2)

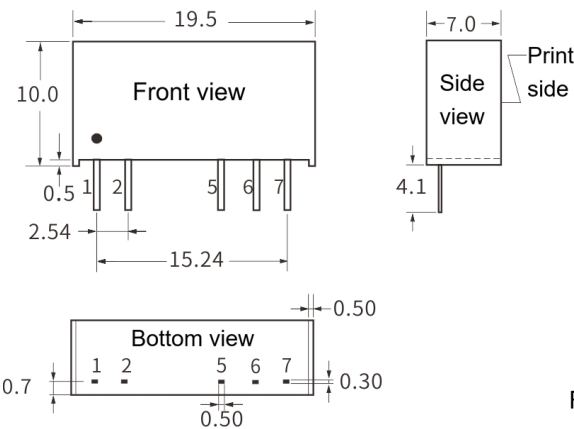
Vin (Vdc)	3.3/5/12/9/15/24
Cin	4.7u F/50V
Cout	See table 1
Lin	4.7uH
Lout	4.7uH

3. Output load requirements

To ensure that this module operates efficiently and reliably, the minimum output load must not be less than 10% of the rated load during use. If your power requirement is indeed very low, please connect a resistor in parallel between the positive and negative terminals of the output (the total power rating of the resistor must be greater than or equal to 10% of the rated power, and the selected resistor's power rating must be at least 5 times greater than the actual power consumption; otherwise, the resistor will become excessively hot).

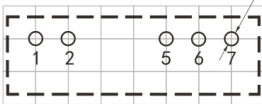
Appearance Dimensions and Pin Descriptions, Recommended PCB Layout

1) Appearance Dimensions (Unit: mm, Tol.: xx ±0.25) 2) Pin Assignment



1	2	3	4	5	6	7
+Vin	-Vin	No Pin	No Pin	-Vout	COM	+Vout
+ input	- input	No pin	No pin	- output	Common ground	+ output

3) Recommended layout



Remarks: grid distance : 2.54*2.54mm

*Note: If the pin assignments of the power module differ from those in the selection guide, refer to the labels on the actual unit.

Package Description

Package code	L x W x H	
S	19.5 x 7.0x 10.0 mm	0.768 × 0.276 ×0.394inch

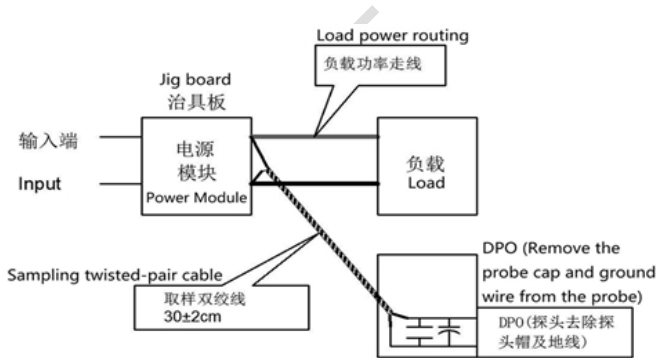
Ripple & Noise Testing: (Twisted-pair method, 20 MHz bandwidth)

Test Method:

1. For ripple and noise testing, use a Category 12 twisted-pair cable. Set the oscilloscope bandwidth to 20 MHz and use a 100 MHz bandwidth probe. Connect a 0.1 μF polypropylene capacitor and a 4.7 μF high-frequency, low-impedance electrolytic capacitor in parallel at the probe tip. Set the oscilloscope to “Sample” mode.

2. Schematic diagram for output ripple and noise testing:

Connect the power supply input to the power source. Connect the power supply output to the electronic load via the fixture board. For testing, use a 30 cm ± 2 cm sampling lead to take a direct sample from the power supply output port. Select insulated wires with appropriate gauge based on the magnitude of the output current.



Application Precautions

1. Input Requirements: Ensure that the output voltage fluctuation range of the power supply does not exceed the input requirements of the DC/DC module itself; the output power of the input power supply must be greater than the output power of the DC/DC module.
2. Recommended Circuit 1: For applications with general ripple and noise requirements, a filter capacitor can be connected in parallel at both the input and output terminals. The external circuit is shown in Figure (1) below; refer to Table (1).
Output Load Requirements: Avoid operating under no-load conditions whenever possible. When the actual power consumption of the load is less than 10% of the module's rated output power or no-load conditions occur, it is recommended to connect a dummy load to the output. The dummy load (resistor) can be calculated based on 5-10% of the module's rated power, where the resistance value = $U_{out} / (1W \times R3 \times 10\%)$;
3. Overload Protection: Under normal operating conditions, the product's output circuit does not provide overload protection. Prolonged overload will trigger over-temperature protection, causing the output to shut down;
4. The output features continuous short-circuit protection with automatic recovery.
5. The capacitance of the external capacitor connected to the output should not be too large, as this may cause overcurrent or poor startup during module startup;
6. If the product operates below the minimum required load, performance compliance with all specifications in this manual cannot be guaranteed;
7. Maximum capacitive loads are tested within the input voltage range and under full-load conditions;
8. Unless otherwise specified, all specifications in this manual are measured at $T_a = 25^\circ \text{C}$, humidity < 75% RH, nominal input voltage, and rated output load;
9. All test methods for the specifications in this manual are based on our company's standards;
10. We offer product customization; please contact our technical or sales staff directly for details;
11. Product specifications are subject to change without notice.

Contact

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