

Typical Characteristics

- Fixed-voltage input, isolated regulated output, 2W power
- Isolation voltage: 1500VDC
- Low no-load power consumption: 0.10W(Typ.)
- Efficiency: up to 76%
- Working ambient temperature: -40℃~+85℃
- MTBF≥3.5 million hours (3500000Hrs)
- Output short-circuit protection: continuous short-circuit protection, automatic recovery
- Compact SIP package, plastic housing
- Industry standard pin-out
- Ripple/noise (20MHz bandwidth): 30mVp-p(Typ.)

2W, fixed-voltage input, isolated regulated dual-output DC/DC power module



Over-temperature protection & continuous output short-circuit protection

RoHS

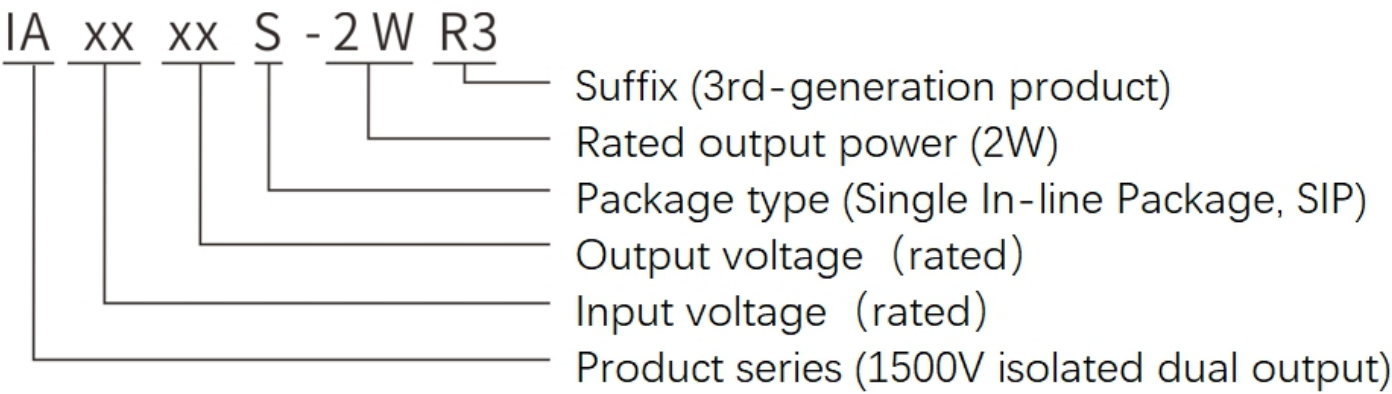
IA_S-2WR3 Series—is a compact, high-efficiency, low-power, fixed-input-voltage, isolated, dual-output DC/DC module power supply designed by Huizhi Electronics for its customers;

This product series is specifically designed for applications in on-board power supply systems that require the generation of a voltage isolated from the input power source.

This product is suitable for:

- applications where the input voltage is relatively stable (voltage variation range: $\pm 5\%$ V_{in});
- applications requiring isolation between input and output (isolation voltage ≤ 1500 VDC);
- applications with high requirements for output voltage stability and low output ripple and noise;
- typical applications: pure digital circuits, general low-frequency analog circuits, relay driver circuits, data switching circuits, etc.

Model Number Description



Product selection table

Certi- fica- tion	Model number	Input voltage range (Vdc)	Output Voltage/Current		Ripple and Noise	Efficiency @ Full load	Max. capaci- tive load
		Rated value ^② (Range)	Output V (Vdc)	Output I (mA) (Max. Min.)	Full Load (mVp-p) Typ. /Max.	% (Min. / Typ.)	μ F
	IA0503S-2WR3	5 (4.75-5.25)	±3.3	200/20	30/80	68/74	1200
	IA0505S-2WR3		±5	200/40	30/80	70/74	1200
	IA0509S-2WR3		±9	111/11	30/80	70/74	680
	IA0512S-2WR3		±12	83/16	30/80	70/74	680
	IA0515S-2WR3		±15	66/6	30/80	70/74	680
	IA0524S-2WR3		±24	42/6	30/80	70/74	680
	IA0903S-2WR3	9 (8.55-9.45)	±3.3	200/20	30/80	68/74	1200
	IA0905S-2WR3		±5	200/40	30/80	70/74	1200
	IA0909S-2WR3	9 (8.55-9.45)	±9	111/11	30/80	70/74	680
	IA0912S-2WR3		±12	83/16	30/80	70/74	680
	IA0915S-2WR3		±15	66/6	30/80	70/74	680
	IA0924S-2WR3		±24	42/6	30/80	70/74	680
	IA1203S-2WR3	12 (11.4-12.6)	±3.3	200/20	30/80	68/74	1200
	IA1205S-2WR3		±5	200/20	30/80	70/74	1200
	IA1209S-2WR3		±9	111/11	30/80	70/74	1200
	IA1212S-2WR3		±12	83/8	30/80	70/74	680
	IA1215S-2WR3		±15	66/6	30/80	70/74	680
	IA1224S-2WR3		±24	42/4	30/80	70/74	680
	IA1503S-2WR3	15 (14.25-15.75)	±3.3	200/20	30/80	68/74	1200
	IA1505S-2WR3		±5	200/20	30/80	70/74	1200
	IA1509S-2WR3		±9	111/11	30/80	70/74	1200
	IA1512S-2WR3		±12	83/8	30/80	70/74	680
	IA1515S-2WR3		±15	66/6	30/80	70/74	680
	IA1524S-2WR3		±24	42/4	30/80	70/74	680
	IA2405S-2WR3	24 (22.8-25.2)	±5	200/40	30/80	70/74	1200
	IA2409S-2WR3		±9	111/11	30/80	70/74	680
	IA2412S-2WR3		±12	83/16	30/80	70/74	680
	IA2415S-2WR3		±15	66/6	30/80	70/74	680
	IA2424S-2WR3		±24	42/6	30/80	70/74	680

Note: 1. Due to space limitations, the above is only a list of representative products. For products not included in this list, please contact our Sales Department.

2. The maximum capacitive load indicates the maximum capacitive load that can be connected to +Vo or -Vo. If this value is exceeded, the product will not start properly.

Test Conditions: Unless otherwise specified, all parameter measurements were taken at the nominal input voltage, with a purely resistive rated load, and at an ambient temperature of 25°C.

Input characteristics

Items	Working conditions	Min.	Typ.	Max.	Unit
Input Current (Full Load/No Load)	5VDC input series	--	540/25	--/35	mA
	9VDC input series	--	317/22	--/30	
	12VDC input series	--	238/20	--/25	
	15VDC input series	--	190/15	--/25	
	24VDC input series	--	119/12	--/20	
Reflected ripple current		--	15	--	mA
Impulse voltage (I sec. max)	5VDC input series	-0.7	--	9	VDC
	9VDC, 12VDC, 15VDC input series	-0.7	--	18	
	24VDC input series	-0.7	--	30	
Input filter type	Capacitor filtering				
Hot swap	Not supported				

Output characteristics

Items	Operating and test conditions	Min.	Typ.	Max.	Unit
Output load	Load percentage	10	--	100	%
Output voltage accuracy		--	--	±3.0	%
Line Regulation	Input voltage variation: ±1%	--	--	±0.25	%
Load Regulation	10%~100% load	--	±1	±2	%
Ripple & Noise	Pure resistive load, 20 MHz bandwidth, peak-to-peak value	--	30	80	mVp-p
Temperature drift coefficient	Full load	--	--	±0.03	%/°C
Output short-circuit protection	Sustainable short-circuit protection, automatic recovery				

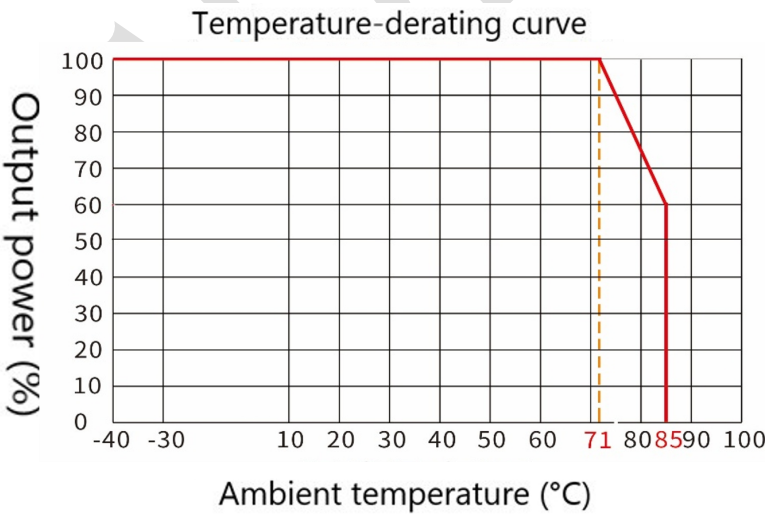
Note: ① Test method for ripple and noise: Twisted-pair test method.

General characteristics

Items	Working conditions	Min.	Typ.	Max.	Unit
Insulation voltage	Input-output, test duration: 1 minute, leakage current less than 1 mA.	1500	--	--	VDC

Insulation resistance	Input-output, insulation voltage 500 VDC.	1000	--	--	MΩ
Isolation capacitor	Input-Output, 100 kHz/0.1 V	--	20	--	pF
Operating temperature	Use the reference temperature derating curve	-40	--	+85	℃
Storage temperature		-40	--	+125	
Maximum operating case temperature		--	25	--	
Storage humidity	No condensation	5	--	95	%RH
Pin soldering temperature resistance	The solder joint is 1.5 mm from the housing; 10 seconds	--	--	+300	℃
Switching frequency	Full load, rated voltage input	--	100	--	KHz
Vibration		10-55Hz, 10G, 30 Min. along X, Y and Z			
Housing material		Black flame-retardant, heat-resistant plastic (UL94 V-0)			
Minimum mean time between failures	MIL-HDBK-217F@25℃	3.5X10 ⁶	--	--	Hrs

Product Performance Curve

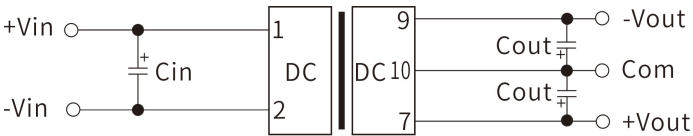


Typical Application Reference Circuit (Recommended Parameters)

1. General applications:

If further reduction of input and output ripple is required, a capacitor filter network can be connected across the input and output terminals; the circuit is shown in Figure 1.

However, care must be taken to select an appropriate filter capacitor. If the capacitor is too large, it is likely to cause startup issues. For each output channel, the recommended capacitive load values—which ensure safe and reliable operation—are listed in Table 1.

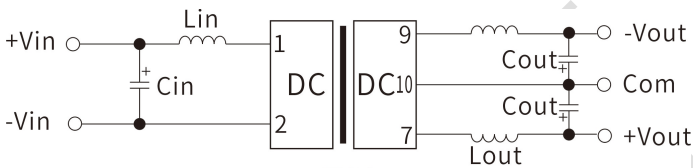


图(1)

Recommended capacitive load values (Table 1)

Vin (Vdc)	Cin (u F)	Vo(Vdc)	Cout (u F)
5	4.7	±5	10
12	2.2	±9	4.7
24	1	±12	2.2
—	—	±15	1

2. Typical EMI application circuits



图(2)

Recommended EMI reference circuit values (Table 2)

Vin (Vdc)	5/12/24
Cin	4.7u F/50V
Cout	See table 1
Lin	4.7uH
Lout	4.7uH

3. Output load requirements

To ensure that this module operates efficiently and reliably, the minimum output load must not be less than 10% of the rated load during use. If your power requirement is indeed very low, please connect a resistor in parallel between the positive and negative terminals of the output (the total power rating of the resistor must be greater than or equal to 10% of the rated power, and the selected resistor's power rating must be at least 5 times greater than the actual power consumption; otherwise, the resistor will become excessively hot).

Appearance Dimensions and Pin Assignment, Recommended PCB Layout

1) Appearance Dimensions

Front view: 27.5 mm width, 12.0 mm height, 0.5 mm pin height, 2.30 ± 0.5 mm pin pitch, 2.54 mm pin spacing, 12.7 mm pin length, 0.7 mm bottom clearance, 0.5 mm bottom pin offset.

Side view: 9.5 mm width, 4.1 mm height, 0.5 mm bottom clearance.

Bottom view: 27.5 mm width, 12.0 mm height, 0.7 mm bottom clearance, 0.5 mm bottom pin offset.

2) Pin Assignment

1	2	7	9	10
+Vin	-Vin	+Vout	-Vout	Com
+ input	- input	+ output	- output	Com. G

3) Recommended layout

Units : mm
Tol.: xx ± 0.25

Remarks: grid distance : 2.54*2.54mm

*Note: If the pin assignment of the power module is different from those in the selection guide, refer to the labels on the actual product.

Package Description

Package code	L x W x H	
S	27.0 x 9.5 x 12.0 mm	1.063 ×0.374 ×0.472inch

Test Application Reference

Ripple & Noise Testing: (Twisted-pair method, 20 MHz bandwidth)

Test Method:

1. For ripple and noise testing, use a Category 12 twisted-pair cable. Set the oscilloscope bandwidth to 20 MHz and use a 100 MHz bandwidth probe. Connect a 0.1 μF polypropylene capacitor and a 4.7 μF high-frequency, low-impedance electrolytic capacitor in parallel at the probe tip. Set the oscilloscope to “Sample” mode.

2. Schematic diagram for output ripple and noise testing:

Connect the power supply input to the power source. Connect the power supply output to the electronic load via the fixture board. For testing, use a 30 cm ± 2 cm sampling lead to take a direct sample from the power supply output port. Select insulated wires with appropriate gauge based on the magnitude of the output current.

Input端 Input

Jig board 治具板

电源模块 Power Module

负载功率走线 负载功率走线

负载 Load

采样双绞线 取样双绞线 30±2cm

DPO (Remove the probe cap and ground wire from the probe) DPO (探头去除探头帽及地线)

Application Precautions

1. Input Requirements: Ensure that the output voltage fluctuation range of the power supply does not exceed the input requirements of the DC/DC module itself; the output power of the input power supply must be greater than the output power of the DC/DC module.
2. Recommended Circuit 1: For applications with general ripple and noise requirements, a filter capacitor can be connected in parallel at both the input and output terminals. The external circuit is shown in Figure (1) below; refer to Table (1). Output Load Requirements: Avoid operating under no-load conditions whenever possible. When the actual power consumption of the load is less than 10% of the module's rated output power or no-load conditions occur, it is recommended to connect a dummy load to the output. The dummy load (resistor) can be calculated based on 5-10% of the module's rated power, where the resistance value = $U_{out} / (1W \times R3 \times 10\%)$;
3. Overload Protection: Under normal operating conditions, the product's output circuit does not provide overload protection. Prolonged overload will trigger over-temperature protection, causing the output to shut down;
4. The output features continuous short-circuit protection with automatic recovery.
5. The capacitance of the external capacitor connected to the output should not be too large, as this may cause overcurrent or poor startup during module startup;
6. If the product operates below the minimum required load, performance compliance with all specifications in this manual cannot be guaranteed;
7. Maximum capacitive loads are tested within the input voltage range and under full-load conditions;
8. Unless otherwise specified, all specifications in this manual are measured at $T_a=25^{\circ}C$, humidity $<75\%$ RH, nominal input voltage, and rated output load;
9. All test methods for the specifications in this manual are based on our company's standards;
10. We offer product customization; please contact our technical or sales staff directly for details;
11. Product specifications are subject to change without notice.

Contact

GDHUIZHI®

广东汇智电子科技有限公司

Guangdong Huizhi Electronic Technology Co., Ltd.

地址：广东省肇庆市端州区 11 区肇庆大道北侧厂房、办公楼(二期)3 楼

Add.: 3rd floor, factory and office building (phase II) on the north side of Zhaoqing Avenue,
District 11, Duanzhou District, Zhaoqing City, Guangdong Province, China

Homepage: www.huizhi-elec.com/www.chinaebizal.com

Tel: +86 - 758 - 2566 585

E-mail: sales@huizhi-elec.com