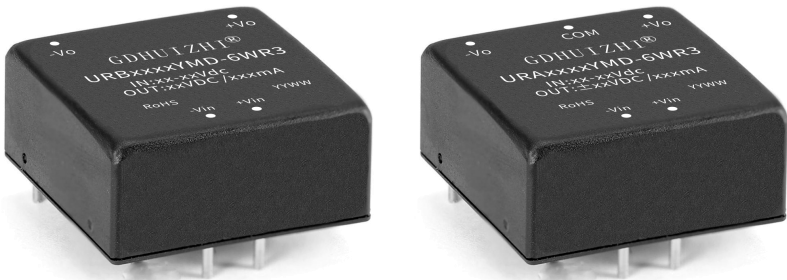


Typical Characteristics

- Wide input range (4:1), Output: 6W
- Conversion efficiency: 81%(Typ.)
- Isolation voltage: 1500Vdc
- Low standby power consumption: 0.3W(Typ.)
- Fast startup: 500ms (Typ.)
- Working temperature: -40℃~+85℃
- Protection: input undervoltage, output short-circuit, overcurrent, overvoltage
- Metal housing, low output ripple
- Industrial standard pin-out, through-hole mounting on PCBs

6W, ultra-wide voltage input, isolated regulated dual/ single output, DIP package, DC/DC power module



RoHS

URB_YMD-6WR3 and URA_YMD-6WR3 series products deliver 6W of output power, feature a 4:1 wide input voltage range, and achieve up to 88% efficiency. a standard isolation voltage of 1500 VDC, an operating temperature range of -40° C to +85° C, and output overcurrent, overload, and short-circuit protection. The bare unit complies with CISPR 32/EN 55032 Class A and is widely used in medical, industrial control, power, instrumentation, communications, and railway applications.

Model Number Description

UR	x	xx	xx	YMD	- 6W	R3	
							Suffix (3rd-generation product)
							Rated output power (6W)
							Package type (25.4 x 25.4 mm)
							Output voltage (rated)
							Input voltage (rated)
							No. of output (B: single, A: dual, 1500 Vdc isolation)
							Product series (UR indicates 4:1 input)

Product selection table

Certification	Model number	Input voltage range (Vdc)		Output Voltage/Current		Ripple and Noise	Maximum capacitive load ^②	Efficiency@ Full load
		Rated (range)	Max ^③	Output V (Vdc)	Output I (mA) (Max. Min.)	Full load (mVp-p) (Typ. /Max.)	μF Max.	% Min. /Typ .
	URB0505YMD-6WR3	5 (4.5-9)	12	5	1200/0	50/80	1000	70/73
	URB0509YMD-6WR3			9	666/0	50/80	470	73/76
	URB0512YMD-6WR3			12	500/0	50/80	470	75/78

URB0515YMD-6WR3			15	400/0	50/80	220	75/78
URB0524YMD-6WR3			24	250/0	50/80	100	75/78
URB2403YMD-6WR3	24 (9-36)	40	3.3	1500/0	50/80	1800	73/77
URB2405YMD-6WR3			5	1200/0	50/80	1000	76/80
URB2412YMD-6WR3			12	500/0	50/80	470	78/81
URB2415YMD-6WR3			15	400/0	50/80	220	78/81
URB2424YMD-6WR3			24	250/0	50/80	100	78/81
URA2405YMD-6WR3			±5	±600/0	50/80	470	76/80
URA2412YMD-6WR3			±12	±250/0	50/80	100	78/81
URA2415YMD-6WR3			±15	±200/0	50/80	100	78/81
URA2424YMD-6WR3			±24	±125/0	50/80	100	78/81
URB4803YMD-6WR3	48 (18-75)	80	3.3	1500/0	50/80	1800	73/77
URB4805YMD-6WR3			5	1200/0	50/80	1000	76/80
URB4812YMD-6WR3			12	500/0	50/80	470	78/81
URB4815YMD-6WR3			15	400/0	50/80	220	78/81
URB4824YMD-6WR3			24	250/0	50/80	100	78/81
URA4805YMD-6WR3			±5	±600/0	50/80	470	76/80
URA4812YMD-6WR3			±12	±250/0	50/80	100	78/81
URA4815YMD-6WR3			±15	±200/0	50/80	100	78/81
URA4824YMD-6WR3			±24	±125/0	50/80	100	78/81
URB1D03YMD-6WR3	110 (40-160)	180	3.3	1400/0	50/80	2500	73/77
URB1D05YMD-6WR3			5	1200/0	50/80	1000	76/80
URB1D12YMD-6WR3			12	500/0	50/80	470	78/81
URB1D15YMD-6WR3			15	400/0	50/80	220	78/81
URB1D24YMD-6WR3			24	250/0	50/80	100	78/81
URA1D05YMD-6WR3			±5	±600/0	50/80	470	76/80
URA1D12YMD-6WR3			±12	±250/0	50/80	100	78/81
URA1D15YMD-6WR3			±15	±200/0	50/80	100	78/81
URA1D24YMD-6WR3			±24	±125/0	50/80	100	78/81

Note: 1. Due to space limitations, the above is only a list of representative products. For products not included in this list, please contact our Sales Department.

2. The maximum capacitive load indicates the maximum capacitive load that can be connected to +Vo or -Vo; if this value is exceeded, the product will not start properly.

3. Exceeding the maximum input voltage may cause permanent damage to the product.

Test Conditions: Unless otherwise specified, all parameter measurements were taken at the rated input voltage, with a purely resistive rated load, and at an ambient temperature of 25℃.

Input characteristics

Items	Working conditions		Min.	Typ.	Max.	Unit
Input Current (Full Load/No Load)	24VDC rated input series, rated input voltage	3.3V	—	268/12	282/25	mA
		Others	—	312/12	328/25	
	48VDC rated input series, rated input		3.3V	—	134/6	141/12

	110VDC rated input series, rated input voltage	Others	-	156/6	162/12	
		3.3V	-	58/3	62/8	
		Others	-	68/3	72/8	
Reflected ripple current	24VDC rated input series, rated input voltage		-	40	-	mA
	48VDC rated input series, rated input voltage		-	30	-	
	110VDC rated input series, rated input voltage		-	25	-	
Impulse voltage (I sec. max)	24VDC rated input series, rated input voltage		-0.7	-	50	VDC
	48VDC rated input series, rated input voltage		-0.7	-	100	
	110VDC rated input series, rated input voltage		-0.7	-	180	
Startup voltage	24VDC rated input series, rated input voltage		-	-	9	
	48VDC rated input series, rated input voltage		-	-	18	
	110VDC rated input series, rated input voltage		-	-	40	
Input undervoltage protection	24VDC rated input series, rated input voltage		-	-	-	
	48VDC rated input series, rated input voltage		-	-	-	
	110VDC rated input series, rated input voltage		-	-	-	
Startup time	Rated input voltage and constant-resistance load		-	500	-	mS
Input filter type			PI type			
Hot swap	-		Not supported			
Remote control (Ctrl) *	Module on		Ctrl floating or connected to a TTL high level (3.5-12VDC)			
	Module off		Connect Ctrl to GND or a low level (0-1.2VDC)			
	Input current during turn-off		-	0	1	mA

Note: *The voltage on the Ctrl pin is relative to the input pin GND.

Output characteristics

Items	Working and test conditions	+Vo1			-Vo2		
		Min.	Typ.	Max.	Min.	Typ.	Max.
Output load	Load percentage	0%	-	100%	0%	-	100%
Output voltage accuracy		-	±1.0%	±2.0%	-	±2.0%	±3.0%
Line Regulation	Input voltage range	-	±0.2%	±0.5%	-	±1.5%	±2%
Load Regulation	20% to 100% rated load, balanced load	-	±0.5%	±1%	-	±4.0%	±5.0%
Ripple & Noise	Pure resistive load, 20 MHz bandwidth, peak-to-peak value	---	±0.02	±0.03	---	±0.02	±0.03
Startup delay		-	50mVp-p	80mVp-p	-	50mVp-p	80mVp-p
Output voltage adjustment	Input voltage range	-	500ms	-	-	500ms	-

Output load	Load percentage	-	Non-adju stable	-	-	Non-adju stable	-
Dynamic response to a step disturbance	25% of the rated load step	-	±3.0%	±5.0%	-	±3.0%	±5.0%
Dynamic response recovery time		-	300 μs	500 μs	-	300 μs	500 μs
Output overvoltage protection	Full voltage range input	-	-	-			
Output overcurrent protection	Full voltage range input	110% Io	150% Io	200% Io			
Output short-circuit protection	Full voltage range input	Sustainable, self-recovering					

Note: ① For product models with output voltages of ±5 VDC and ±9 VDC, the maximum output voltage accuracy under 0% - 5% load conditions is ±5%;

② When tested under 0% - 5% load conditions, the load regulation is ±5%;

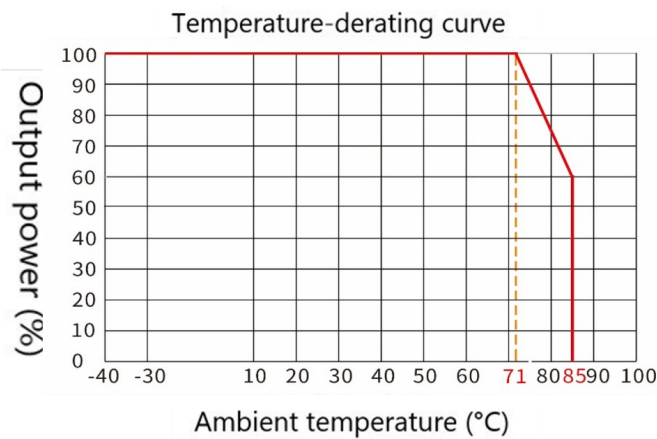
③ The load ripple and noise at 0% - 5% load is less than or equal to 5% of Vo. The test method for ripple and noise is the twisted-pair test; a capacitive load can be applied to the output to reduce light-load ripple.

General characteristics

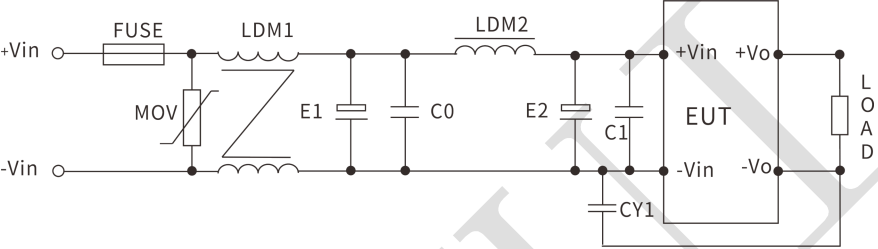
Items	Working conditions	Min.	Typ.	Max.	Unit
Insulation voltage	Input-output, test duration: 1 minute, leakage current less than 1 mA.	1500	--	--	VDC
Insulation resistance	Input-output, insulation voltage 500 VDC.	1000	--	--	MΩ
Isolation capacitor	Input-Output, 100 kHz/0.1 V	--	1000	--	pF
Working temperature	Use the reference temperature derating curve	-40	--	+85	℃
Storage temperature		-40	--	+125	
Maximum operating case temperature		--	--	+100	
Storage humidity	No condensation	5	--	95	%RH
Pin soldering temperature resistance	The solder joint is 1.5 mm from the housing; 10 seconds	--	--	+300	℃
Switching frequency	PWM mode	--	250	--	KHz

Vibration		10-55Hz, 10G, 30 Min. along X, Y and Z			
Housing material		Aluminum alloy housing			
Minimum mean time between failures	MIL-HDBK-217F@25°C	--	2X10 ⁵	--	Hrs

Temperature Characteristic Curve



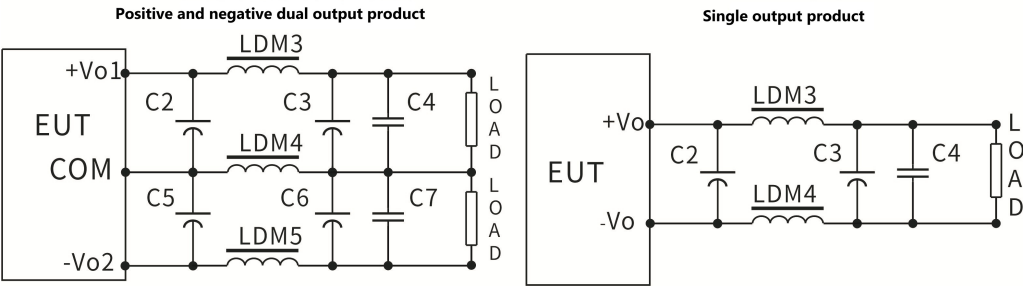
EMC Peripheral Recommended Circuit



Recommended parameters: The following are typical settings; please adjust them accordingly based on your specific operating environment.

Component code	24V Input	48V Input	110V Input
FMSE fuse	Connect the appropriate fuse based on customer requirements		
MOV varistor	14D560K	14D101K	14D201K
LDM1 common-mode inductor	10 mH	15 mH	30 mH
E1、E2 electrolytic capacitor	100 μ F/50V	100 μ F/100V	63 μ F/200V
C0、D1 ceramic capacitor	1 μ F/50V	1 μ F/100V	0.47 μ F/250V
LDM2 common-mode inductor	10 μ H	15 μ H	68 μ H
CY1 safety Y2 capacitors	1nF/250Vac		

Recommended Peripheral Circuit for Output Filtering



For general ripple and noise requirements, it is recommended to use only C2 and C5 in the peripheral circuit; for strict ripple and noise requirements, it is recommended to use the circuit shown in the figure above.

Note: 1. Use high-frequency, low-impedance electrolytic capacitors for C2, C3, C5, and C6. The total capacitance must not exceed the maximum capacitive load specified in the manual; otherwise, the module will fail to start normally.

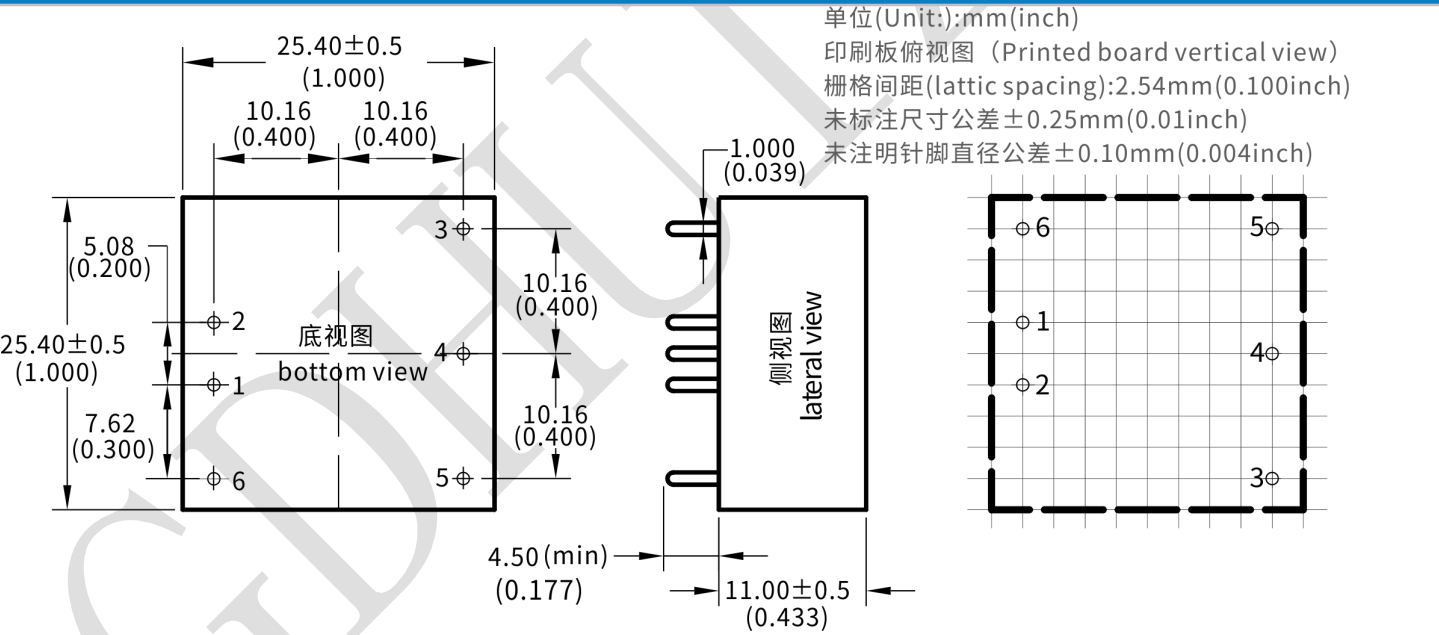
2. When applying a capacitive load, ensure that the minimum load is 3%; otherwise, it will cause abnormal module output.

3. The LDM5 is intended for use only in dual-output products.

Recommended parameters:

Component code	3.3V Output	±5V or 5V Output	±9V/12V or 9V/12V Output	±15V or 15V Output	±24V or 24V Output
LDM3 inductor	0.47 μH	1 μH	2.2 μH	2.2 μH	4.7 μH
LDM4 inductor	0.47 μH	1 μH	2.2 μH	2.2 μH	4.7 μH
LDM5 inductor	—	1 μH	2.2 μH	2.2 μH	4.7 μH
C2, C3 electrolytic capacitor	220 μF	220 μF	100 μF	100 μF	68 μF
C5, C6 electrolytic capacitor	220 μF	220 μF	100 μF	100 μF	68 μF
C4, C7 ceramic capacitor	1 μF/50V				

Package Dimensions and Pin Function Diagram



	1	2	3	4	5	6
Single (S)	-Vin	+Vin	+Vo	NP	-Vo	NP
	Negative input	Positive input	Positive output	No pin	Output ground	No pin
Dual (D)	-Vin	+Vin	+Vo	COM	-Vo	NP
	Negative input	Positive input	Positive output	Common	Negative output	No pin

*Note: If the pin definitions of the power module differ from those in the selection guide, refer to the labels on the actual product.

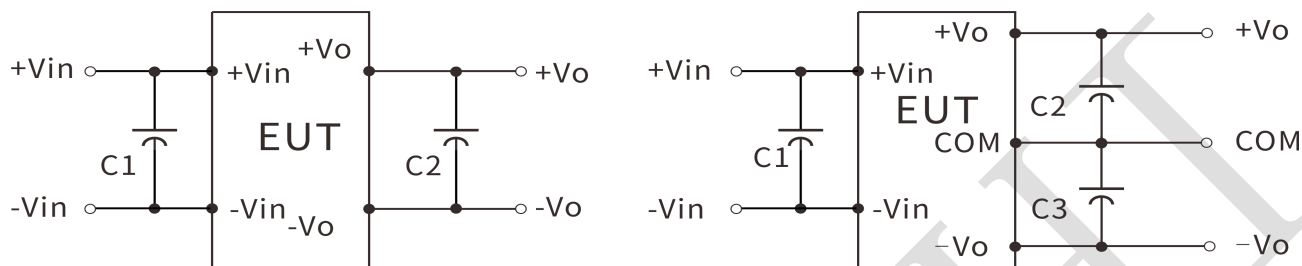
Package Description

Package code	L x W x H	
YMD	25.4 X25.4X11.0mm	1.000X 1.000 X0.433 inch

Test Application Reference

Recommended Test Circuit 1、DC/DC Test Circuit:

Recommended capacitors: C1: 47-100 μ F; C2、C3: 10-22 μ F 。



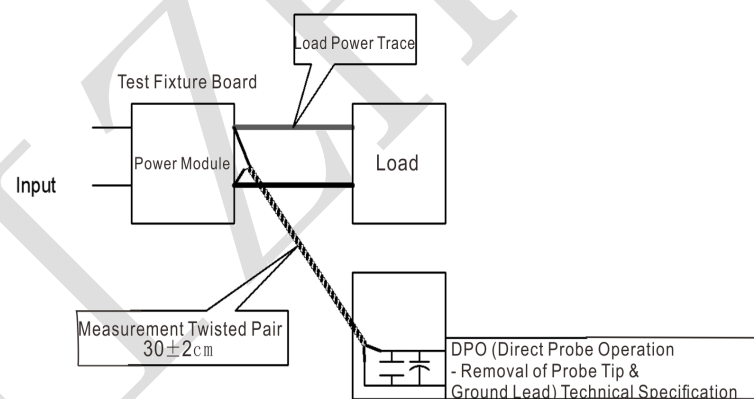
2. Ripple & Noise Testing: (Twisted-pair Method, 20 MHz Bandwidth)

Test Method:

1. For ripple and noise testing, use a Category 12 twisted-pair cable. Set the oscilloscope bandwidth to 20 MHz and use a 100 MHz bandwidth probe. Connect a 0.1 μ F polypropylene capacitor and a 47 μ F high-frequency, low-impedance electrolytic capacitor in parallel at the probe tip. Set the oscilloscope to “Sample” mode.
2. Schematic diagram for output ripple and noise testing:

Connect the power supply input to the input power

source. Connect the power supply output to the electronic load via the fixture board. For testing, use a 30 cm $\pm$ 2 cm sampling lead to take a direct sample from the power supply output port. Select insulated wires with appropriate gauge based on the magnitude of the output current.



Contact

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